Annexure – I

E.G.S. PILLAY ENGINEERING COLLEGE

(An Autonomous Institution, Affiliated to Anna University, Chennai)
Nagore Post, Nagapattinam – 611 002, Tamilnadu.

Board of Studies Meeting

Attendance

Name of the Department : Electronics and Communication Engineering
Meeting No : 01
Date & Time : 08-07-2017 & 9.30 A.M.
Venue : ECE A/C Seminar Hall

Sl.No.	Name	Category	Signature
1	Dr. S. M. RAMESH	Chairman	8/7/17
2	Dr. MALA JOHN	University Nominee	8/7/17
3	Dr. M. BHASKAR	External Expert Members	8/7/17
4	Dr. J. RAMESH		8/7/17
5	Dr. N. B. BALAMURUGAN		
6	Mr. V. UDAYASANKAR	Industry / Corporate Sector	
7	Mr. J. MUTHUKUMAR	Alumnus	
8	Mr. B. DHAKSHINAMOORTHY		
9	Dr. A. RAJARAM	Internal Members	
10	Prof. R. LAKSHMIREKHA		
11	Prof. RS.KOTEESHWARI		8/7/17
12	Prof. M. VIJAYAKUMAR		08/07/17

8/7/17

Chairman – BoS (ECE)

E.G.S. PILLAY ENGINEERING COLLEGE

(An Autonomous Institution, Affiliated to Anna University, Chennai)
Nagore Post, Nagapattinam – 611 002, Tamilnadu.

Minutes of Board of Studies (BoS) Meeting

Name of the Department	Electronics and Communication Engineering.
Names of the Programmes	B.E - Electronics and Communication Engineering. M.E - Communication Systems.
Meeting No	01
Date & Time	8.7.2017
Venue	ECE Seminar Hall

The following are the suggestions given by the member of Board of Studies.

- The Board of Studies members have gone through the curriculum and syllabi of first and second semester B.E. Electronics and Communication Engineering and M.E. Communication Systems Programmes.
- Language Electives –II can be renamed as English II and Electives can be removed
- Standard Text books can be mentioned separately in the syllabus, Textbooks (UG Course only) and it should cover at least 4 units. Reference books can be given separately
- Mathematics III can be replaced by Partial differential equations and Linear Algebra as transform concepts are covered in Signals and System Course
- Total number of credits for B.E - ECE programme can be fixed (184) as low.

8/7/17

Chairman – BOS (ECE)

E.G.S. PILLAY ENGINEERING COLLEGE

(An Autonomous Institution)

Approved by AICTE, New Delhi | Affiliated to Anna University, Chennai

Accredited by NAAC with 'A' Grade | An ISO 9001 : 2008 Certified Institution

NAGAPATTINAM – 611002

B.E. ELECTRONICS AND COMMUNICATION ENGINEERING
REGULATIONS - 2017
CHOICE BASED CREDIT SYSTEM
CURRICULUM FOR I TO VIII SEMESTERS
Minimum Credits to be Earned :184

B.E. ELECTRONICS AND COMMUNICATION ENGINEERING

FIRST SEMESTER

Course Code	Course Title	Objectives & Outcomes		L	T	P	C	Maximum Marks			Category
		PEOs	POs					CIA	ESE	Total	
	English - I			3	0	0	3	40	60	100	HSS
	Engineering Mathematics-I			2	2	2	4	40	60	100	BS
	Applied Physics			3	0	2	4	50	50	100	BS
	Applied Chemistry			3	0	2	4	50	50	100	BS
	Basics of Electrical and Instrumentation Engineering			3	0	2	4	50	50	100	ES
	Programming in C			3	0	2	4	50	50	100	ES
	Engineering Graphics			3	0	2	4	50	50	100	ES
(Contact Periods: 36)			**Total**	**20**	**4**	**12**	**27**	**320**	**380**	**700**	-

SECOND SEMESTER

Course Code	Course Title	Objectives & Outcomes		L	T	P	C	Maximum Marks			Category
		PEOs	POs					CIA	ESE	Total	
	English - II			3	0	0	3	40	60	100	HSS
	Engineering Mathematics II			2	2	2	4	40	60	100	BS
	Semiconductor Physics and Devices			3	0	0	3	50	50	100	BS
	Environmental Science			3	0	0	3	50	50	100	HSS
	Basics of Civil and Mechanical Engineering			4	0	0	4	40	60	100	ES
	Circuit Theory			3	2	0	4	40	60	100	ES
	Engineering Practice			0	0	4	2	50	50	100	ES
(Contact Periods: 32)			**Total**	**18**	**4**	**10**	**23**	**320**	**380**	**700**	-

B.E. ELECTRONICS AND COMMUNICATION ENGINEERING

THIRD SEMESTER

Course Code	Course Title	Objectives & Outcomes		L	T	P	C	Maximum Marks			Category
		PEOs	POs					CIA	ESE	Total	
	Partial Differential Equations and Linear Algebra			3	2	0	4	40	60	100	BS
	Data Structures and C++			3	0	2	4	50	50	100	ES
	Network Analysis and Synthesis			3	2	0	4	40	60	100	ES
	Engineering Electromagnetics			3		0	3	40	60	100	ES
	Digital Circuits and Systems			3	0	0	3	40	60	100	PC
	Electronics Circuits			3	0	0	3	40	60	100	PC
	Digital Electronics Laboratory			0	0	4	2	50	50	100	PC
	Electronic Circuits Laboratory			0	0	4	2	50	50	100	PC
	Life Skills: Business English			0	0	2	-	100	-	100	EEC
(Contact Periods: 38)			**Total**	**18**	**6**	**14**	**25**	**500**	**500**	**1000**	-

FOURTH SEMESTER

Course Code	Course Title	Objectives & Outcomes		L	T	P	C	Maximum Marks			Category
		PEOs	POs					CIA	ESE	Total	
	Probability and Random Processes			3	2	0	4	40	60	100	BS
	Signals and Systems			3	2	0	4	40	60	100	PC
	Analog integrated circuits			3	0	0	3	40	60	100	PC
	Microprocessors and Microcontrollers			3	0	0	3	40	60	100	PC
	Transmission Lines and Waveguides			3	0	0	3	40	60	100	PC
	Control Systems			3	0	0	3	40	60	100	PC
	Analog integrated circuits laboratory			0	0	4	2	50	50	100	PC
	Microprocessors and Microcontrollers Laboratory			0	0	4	2	50	50	100	PC
	Life Skills: Verbal Ability			0	0	2	-	100	-	100	EEC
(Contact Periods: 38)			**Total**	**18**	**8**	**12**	**24**	**490**	**510**	**1000**	-

8/7/17 8/7/17 8/7/17 8/7/17

B.E. ELECTRONICS AND COMMUNICATION ENGINEERING

FIFTH SEMESTER

Course Code	Course Title	Objectives & Outcomes		L	T	P	C	Maximum Marks			Catego
		PEOs	POs					CIA	ESE	Total	
	Analog Communication			3	0	0	3	40	60	100	PC
	Antenna and wave propagation			3	0	0	3	40	60	100	PC
	Digital Signal Processing			3	2	0	4	50	50	100	PC
	Computer Networks			3	0	0	3	40	60	100	PC
	Professional Elective - I			3	0	0	3	40	60	100	PE
	Professional Elective - II			3	0	0	3	40	60	100	PE
	Digital Signal Processing Laboratory			0	0	4	2	50	50	100	PC
	Analog communication Laboratory			0	0	4	2	50	50	100	PC
	Technical seminar			0	0	2	1	100	-	100	EEC
	Life Skills: Aptitude - I			0	0	2	-	100	-	100	EEC
(Contact Periods: 34)			**Total**	**18**	**2**	**14**	**24**	**550**	**450**	**1000**	-

SIXTH SEMESTER

Course Code	Course Title	Objectives & Outcomes		L	T	P	C	Maximum Marks			Catego
		PEOs	POs					CIA	ESE	Total	
	Professional Ethics			3	0	0	3	40	60	100	HSS
	VLSI Design			3	0	0	3	40	60	100	PC
	Digital communication			3	0	0	3	40	60	100	PC
	Wireless Networks and Standards			3	0	0	3	40	60	100	PC
	Professional Elective - III			3	0	0	3	40	60	100	PE
	Open Elective - I			3	0	0	3	40	60	100	OE
	VLSI Design Laboratory			0	0	4	2	50	50	100	PC
	Communication and Networks Laboratory			0	0	4	2	50	50	100	PC
	Life Skills: Aptitude - II			0	0	2	-	100	-	100	EEC
(Contact Periods: 32)			**Total**	**18**	**0**	**12**	**22**	**540**	**460**	**1000**	-

8/7/17 8/7/17 8/7/17 8/7/17

B.E. ELECTRONICS AND COMMUNICATION ENGINEERING

SEVENTH SEMESTER

Course Code	Course Title	Objectives & Outcomes		L	T	P	C	Maximum Marks			Category
		PEOs	POs					CIA	ESE	Total	
	Industrial Economics			3	0	0	3	40	60	100	HSS
	Optical Communication			3	0	0	3	40	60	100	PC
	Microwave Electronics			3	0	0	3	40	60	100	PC
	Wireless Communication			3	0	0	3	50	50	100	PC
	Professional Elective - IV			3	0	0	3	40	60	100	PE
	Open Elective - II			3	0	0	3	40	60	100	OE
	Microwave and Optical Communication Laboratory			0	0	4	2	50	50	100	PC
	Mini Project - I			0	0	2	1	100	-	100	EEC
	Life Skills : Competitive Exams			0	0	2	-	100	-	100	EEC
(Contact Periods: 32)			**Total**	**18**	**0**	**8**	**21**	**550**	**450**	**1000**	

EIGHT SEMESTER

Course Code	Course Title	Objectives & Outcomes		L	T	P	C	Maximum Marks			Category
		PEOs	POs					CIA	ESE	Total	
	Professional Elective - V	-	-	3	0	0	3	40	60	100	PE
	Professional Elective - VI	-	-	3	0	0	3	40	60	100	PE
	Professional Elective - VII	-	-	3	0	0	3	40	60	100	PE
	Project Work	1,2,3	1-12	0	0	18	9	50	50	100	EEC
(Contact Periods: 27)			**Total**	**9**	**0**	**18**	**18**	**170**	**230**	**400**	-

SUMMARY OF CREDIT DISTRIBUTION

S.No	CATEGORY	CREDITS PER SEMESTER								TOTAL CREDIT	CREDITS in %
		I	II	III	IV	V	VI	VII	VIII		
1	HSS	3	6	-	-	-	3	3	-	15	6
2	BS	12	7	4	4	-	-	-	-	27	14
3	ES	12	10	11	-	-	-	-	-	33	19
4	PC	-	-	10	20	17	13	11	-	71	40
5	PE	-	-	-	-	6	3	3	9	21	11
6	OE	-	-	-	-	-	3	3	-	06	3
7	EEC	-	-	-	-	1	-	1	9	11	7
	Total	27	23	25	24	24	22	21	18	184	100

E.G.S.PILLAY ENGINEERING COLLEGE (AUTONOMOUS)
NAGAPATTINAM

DEPARTMENT OF
ELECTRONICS AND COMMUNICATION ENGINEERING

ELECTIVES

DISCIPLINE ELECTIVES		L	T	P	C
	NANO ELECTRONICS	3	0	0	3
	ROBOTIC VISION	3	0	0	3
	EMBEDDED PROCESSORS AND NETWORKS	3	0	0	3
	AUTOMOTIVE ELECTRONICS	3	0	0	3
	BIOMEDICAL ENGINEERING	3	0	0	3
	ADVANCED DIGITAL SIGNAL PROCESSING	3	0	0	3
	IMAGE PROCESSING	3	0	0	3
	PATTERN RECOGNITION AND MACHINE LEARNING	3	0	0	3
	ADVANCED WIRELESS MOBILE COMMUNICATIONS	3	0	0	3
	WIRELESS AD-HOC AND SENSOR NETWORKS	3	0	0	3
	MOBILE COMPUTING	3	0	0	3
	GRID COMPUTING	3	0	0	3
	NETWORK SECURITY	3	0	0	3
	SATELLITE COMMUNICATION	3	0	0	3
	RF SYSTEM DESIGN	3	0	0	3
	RF MEMS	3	0	0	3
	BIO MEMS	3	0	0	3
	RADAR AND NAVIGATION AIDS	3	0	0	3
	MEASUREMENT AND INSTRUMENTATION	3	0	0	3
	SYSTEM-ON CHIP DESIGN	3	0	0	3
	NETWORK ON CHIP DESIGN	3	0	0	3
	SPEECH PROCESSING	3	0	0	3
	PYTHON PROGRAMMING AND .NET	3	0	0	3
	MICROWAVE INTEGRATED CIRCUITS	3	0	0	3
	LOW POWER VLSI DESIGN	3	0	0	3
	ANALOG VLSI	3	0	0	3
	FOUNDATION SKILLS IN INTEGRATED PRODUCT DEVELOPMENT	3	0	0	3
	CLOUD COMPUTING	3	0	0	3
	INTERNET OF THINGS (IoT)	3	0	0	3
	VIRTUAL INSTRUMENTATION	3	0	0	3
	EMBEDDED SYSTEM DESIGN	3	0	0	3
	SOFT COMPUTING	3	0	0	3
	BIG DATA ANALYTICS	3	0	0	3
	OPERATING SYSTEMS	3	0	0	3
	REAL TIME OPERATING SYSTEMS	3	0	0	3

8/7/17 8/7/17 8/7/17 8/7/17

E.G.S Pillay Engineering College
An Autonomous Institution, An ISO 9001-2008 Certified Institution
Accredited by NAAC with 'A' Grade, Accredited by NBA (CSE, EEE & MECH)
Approved by AICTE - New Delhi, Affiliated by Anna University - Chennai
Nagapattinam – 611002.

M.E. Communication Systems (Full Time)

Regulation 2017 (R2017)

Minimum Credits to be Earned :76

First Semester

Sl.No	Course Code	Course Title	L	T	P	C
1.		Applied Engineering Mathematics	3	2	0	4
2.		Advanced Digital Signal Processing	3	2	0	4
3.		High Speed Communication Networks	3	0	0	3
4.		Advanced Digital Communication	3	0	0	3
5.		Wireless Communication Engineering	3	0	0	3
6.		Elective -I	3	0	0	3
7.		Signals Processing and Communication Laboratory	0	0	4	2
8.		Wireless Communication Networks Lab	0	0	4	2
9.		Business English-I	1	0	2	2
		Total	**19**	**4**	**10**	**26**
Total Contact Hours/Week			**33**			

Second Semester

Sl.No	Course Code	Course Title	L	T	P	C
1.		FPGA Based Communication System Design	3	0	0	3
2.		Microwave Integrated Circuits	3	0	0	3
3.		Optical Switching and Networking	3	0	0	3
4.		Information Theory and Coding	3	0	0	3
5.		Elective – II	3	0	0	3
6.		Elective – III	3	0	0	3
7.		RF System Design Laboratory	0	0	4	2
8.		Technical Seminar	0	0	2	1
9.		Business English-II	1	0	2	2
		Total	**19**	**0**	**8**	**23**
Total Contact Hours/Week			**27**			

8/7/17 8/7/17 8/7/17

Third Semester

Sl.No	Course Code	Course Title	L	T	P	C
1.		Elective IV	3	0	0	3
2.		Elective V	3	0	0	3
3.		Elective VI	3	0	0	3
4.		Project Work Phase -I	0	0	12	6
		Total	**9**	**0**	12	**15**
		Total Contact Hours/Week	**21**			

Fourth Semester

Sl.No	Course Code	Course Title	L	T	P	C
1.		Project Work Phase -II	0	0	24	12
		Total	**0**	0	24	**12**
		Total Contact Hours/Week	**12**			

8/7/17 8/7/17 8/7/17 8/7/17

List of Electives:

S.No	Course Code	Course Title	T	T	P	C
1.		Advanced Digital Image processing	3	0	0	3
2.		Cognitive Radio	3	0	0	3
3.		Multimedia Communication	3	0	0	3
4.		Wireless Sensor Networks	3	0	0	3
5.		Communication Protocol engineering	3	0	0	3
6.		High speed Switching Architecture	3	0	0	3
7.		Radar and Navigational Aids	3	0	0	3
8.		RF MEMS	3	0	0	3
9.		Advanced Wireless Networks	3	0	0	3
10.		Satellite Communication	3	0	0	3
11.		Network Routing Algorithms	3	0	0	3
12.		Advanced Wireless Communication Engineering	3	0	0	3
13.		Data Converters	3	0	0	3
14.		Electromagnetic Interference and Compatibility in System Design	3	0	0	3
15.		Evolutionary Computing	3	0	0	3
16.		Microwave Remote Sensing	3	0	0	3
17.		Database Management System	3	0	0	3
18.		Speech and Audio Signal Processing	3	0	0	3
19.		RF System Design for Wireless Communication	3	0	0	3
20.		Mobile Adhoc Networks	3	0	0	3
21.		Advanced Fiber Optic Technologies	3	0	0	3
22.		Communication Network Security	3	0	0	3
23.		Telecommunication System Modeling and Simulation	3	0	0	3
24.		Advanced Microwave Communication	3	0	0	3
25.		Space Time Communication	3	0	0	3
26.		Advanced Display System	3	0	0	3
27.		Software Defined Radio	3	0	0	3
28.		Machine Learning Algorithms	3	0	0	3
29.		Analog VLSI System Design	3	0	0	3

One Credit Courses

1. **Analog Communication System Design**
2. **Embedded System Design using MSP430**

E.G.S Pillay Engineering College

An Autonomous Institution, An ISO 9001-2008 Certified Institution
Accredited by NAAC with 'A' Grade, Accredited by NBA (CSE, EEE & MECH)
Approved by AICTE - New Delhi, Affiliated by Anna University - Chennai
Nagapattinam – 611002.

M.E. Communication Systems (Part time)
Regulation 2017 (R2017)

Minimum Credits to be Earned :76

First Semester

Sl.No	Course Code	Course Title	L	T	P	C
1.		Advanced Engineering Mathematics	3	2	0	4
2.		Advanced Digital Signal Processing	3	2	0	4
3.		Advanced Digital Communication	3	0	0	3
4.		Signals Processing and Communication Laboratory	0	0	4	2
5.		Business English-I	1	0	2	2
		Total	10	4	**6**	**15**
Total Contact Hours/Week			**20**			

Second Semester

Sl.No	Course Code	Course Title	L	T	P	C
1.		FPGA Based Communication System	3	0	0	3
2.		Microwave Integrated Circuits	3	0	0	3
3.		Optical Switching and Networking	3	0	0	3
4.		RF System Design Lab	0	0	4	2
5.		Business English-II	1	0	2	2
		Total	**10**	**0**	6	**13**
Total Contact Hours/Week			**16**			

Third Semester

Sl.No	Course Code	Course Title	L	T	P	C
1.		High Speed Communication Networks	3	0	0	3
2.		Wireless Communication Engineering	3	0	0	3
3.		Elective -I	3	0	0	3
4.		Wireless Communication Networks Lab	0	0	4	2
		Total	**9**	0	**4**	**11**
Total Contact Hours/Week			**13**			

8/7/17 8/7/17 8/7/17 8/7/1

Fourth Semester

Sl.No	Course Code	Course Title	L	T	P	C
1.		Information Theory and Coding	3	0	0	3
2.		Elective – II	3	0	0	3
3.		Elective – III	3	0	0	3
8.		Technical Seminar	0	0	2	1
		Total	**9**	**0**	**2**	**10**
Total Contact Hours/Week			**11**			

Fifth Semester

Sl.No	Course Code	Course Title	L	T	P	C
1.		Elective IV	3	0	0	3
2.		Elective V	3	0	0	3
3.		Elective VI	3	0	0	3
4.		Project Work Phase -I	0	0	12	6
		Total	**9**	**0**	12	**15**
		Total Contact Hours/Week	**21**			

Sixth Semester

Sl.No	Course Code	Course Title	L	T	P	C
1.		Project Work Phase -II	0	0	24	12
		Total	**0**	0	24	**12**
		Total Contact Hours/Week	**24**			

8/7/17

8/7/17

8/7/17

8/7/17

List of Electives:

S.No	Course Code	Course Title	T	T	P	C
1.		Advanced Digital Image processing	3	0	0	3
2.		Cognitive Radio	3	0	0	3
3.		Multimedia Communication	3	0	0	3
4.		Wireless Sensor Networks	3	0	0	3
5.		Communication Protocol engineering	3	0	0	3
6.		High speed Switching Architecture	3	0	0	3
7.		Radar and Navigational Aids	3	0	0	3
8.		RF MEMS	3	0	0	3
9.		Advanced Wireless Networks	3	0	0	3
10.		Satellite Communication	3	0	0	3
11.		Network Routing Algorithms	3	0	0	3
12.		Advanced Wireless Communication Engineering	3	0	0	3
13.		Data Converters	3	0	0	3
14.		Electromagnetic Interference and Compatibility in System Design	3	0	0	3
15.		Evolutionary Computing	3	0	0	3
16.		Microwave Remote Sensing	3	0	0	3
17.		Database Management System	3	0	0	3
18.		Speech and Audio Signal Processing	3	0	0	3
19.		RF System Design for Wireless Communication	3	0	0	3
20.		Mobile Adhoc Networks	3	0	0	3
21.		Advanced Fiber Optic Technologies	3	0	0	3
22.		Communication Network Security	3	0	0	3
23.		Telecommunication System Modeling and Simulation	3	0	0	3
24.		Advanced Microwave Communication	3	0	0	3
25.		Space Time Communication	3	0	0	3
26.		Advanced Display System	3	0	0	3
27.		Software Defined Radio	3	0	0	3
28.		Machine Learning Algorithms	3	0	0	3
29.		Analog VLSI System Design	3	0	0	3

One Credit Courses

1. **Analog Communication System Design**
2. **Embedded System Design using MSP430**

8/7/17 8/7/17 8/7/17 8/7/17

E.G.S. PILLAY ENGINEERING COLLEGE
(An Autonomous Institution, Affiliated to Anna University, Chennai)
Nagore Post, Nagapattinam – 611 002, Tamilnadu.

Minutes of Board of Studies (BoS) Meeting

Name of the Department	Electronics and Communication Engineering
Name of the Programmes	B.E. Electronics and Communication Engineering M.E. Communication System
Meeting No.	01
Date & Time	08.07.2017 & 10.00 A.M.
Venue	ECE A/C Seminar Hall

1. Chairman / BoS welcomed all members for the 1st meeting of Board of Studies.

2. Chairman / BoS introduced the members.

3. Chairman / BoS briefed the members about the Agenda.

   - To finalize the curriculum and syllabi of first and second semester B.E Electronics and Communication Engineering Programmes and 1st to 4th Semesters M.E. Communication Systems Programmes.
   - To finalize the curriculum of 3rd – 8th Semester B.E Electronics and Communication Engineering.
   - To discuss the question paper pattern for UG/PG Programme and Evaluation methodologies.
   - Any other matters.

4. The following are the suggestions given by the member of Board of Studies.

   - The Board of Studies members have gone through the curriculum and syllabi of first and second semester B.E Electronics and Communication Engineering and M.E Communication Systems Programmes.
   - The Board of Studies members have gone through the curriculum of 3rd - 8th Semester of B.E Electronics and Communication Engineering and 3rd & 4th Semesters of M.E Communication system Programmes.
   - Total number of credits for BE Electronics and Communication Engineering programme should be with in 184 and total number of credits for M.E Communication Systems programme should be with in 76.

3. RF lab is renamed as RF system design laboratory.

4. Communicative English-II is renamed as Business English-II

3rd Sem:

1. Various new Electives were included based on new technologies.

4th Sem:

1.Project work

Question Paper Patterns

UG Programme.

Part A – 10 x 1 = 10 Marks
Part B – 10 x 2 = 20 Marks
Part C – 5 x 14 = 70 Marks (Either or)
Total = 100 Marks, Duration = 3 Hours

Evaluation Methods

Theory	**Practical**
Continuous Assessment = 40%	Continuous Assessment = 50%
Semester Examinations = 60%	Semester Examinations = 50%

5. The following resolutions were passed

- It is resolved to recommend the curriculum and syllabi of first and second semester of B.E Electronics and Communication Engineering and M.E Communication Systems Programmes to the Academic Council for approval.
- It is resolved to recommend the curriculum of 3rd – 8th Semester B.E Electronics and Communication Engineering and 3rd and 4th semester M.E Communication system Programmes to the academic Council for approval.
- It is resolved to recommend the question paper pattern UG/PG Programmes and evaluation methodology for approval.

The Chairman, Board of Studies thanked all the members for their valuable suggestions and coordination.

Date : 8.7.2017

Place : Nagapattinam

8/7/17

Chairman – BoS (ECE)

E.G.S. PILLAY ENGINEERING COLLEGE

(An Autonomous Institution, Affiliated to Anna University, Chennai)
Nagore Post, Nagapattinam – 611 002, Tamilnadu.

Minutes of Board of Studies(BoS) Meeting

Name of the Department	Electronics and Communication Engineering
Name of the Programmes	B.E. Electronics and Communication Engineering M.E. Communication System
Meeting No.	01
Date & Time	08.07.2017 & 10.00 A.M.
Venue	ECE A/C Seminar Hall

1. Chairman / BoS welcomed all members for the 1st meeting of Board of Studies.
2. Chairman / BoS introduced the members.
3. Chairman / BoS briefed the members about the Agenda.
   - To finalize the curriculum and syllabi of first and second semester B.E ECE Programmes and 1st to 4th Semesters M.E. Communication system Programmes.
   - To finalize the curriculum of 3rd – 8th Semester B.E ECE.
   - To discuss the question paper pattern for UG/PG Programme and Evaluation methodologies.
   - Any other matters.
4. The following are the suggestions given by the member of Board of Studies.

U.G Cerriculam for B.E & M.E has been reviewe and modified Suitably. & the necessary Chang have been incorporated (enclosed).

8/7/17
Chairman (BoS)

University Nominee

Subject Expert

Subject Expert 08/7/17

Industry Expert

Alumni

Senior Faculty

08/07/17
Senior Faculty

Senior Faculty

E.G.S. PILLAY ENGINEERING COLLEGE
(An Autonomous Institution, Affiliated to Anna University, Chennai)
Nagore Post, Nagapattinam – 611 002, Tamil Nadu.

Minutes of Board of Studies (BoS) Meeting

Name of the Department	Electronics and Communication Engineering
Name of the Programmes	B.E. Electronics and Communication Engineering M.E. Communication Systems
Meeting No.	01
Date & Time	08.07.2017 & 10.00 A.M.
Venue	ECE A/C Seminar Hall

1. Chairman / BoS welcomed all members for the 1st meeting of Board of Studies.
2. Chairman / BoS introduced the members.
3. Chairman / BoS briefed the members about the Agenda.
   - To finalize the curriculum and syllabi of first and second semester B.E Electronics and Communication Engineering Programmes and 1st to 4th Semesters M.E. Communication Systems Programmes.
   - To finalize the curriculum of 3rd – 8th Semester B.E Electronics and Communication Engineering.
   - To discuss the question paper pattern for UG/PG Programme and Evaluation methodologies.
   - Any other matters.
4. The following are the suggestions given by the member of Board of Studies.
   - The Board of Studies members have gone through the curriculum and syllabi of first and second semester B.E Electronics and Communication Engineering and M.E Communication Systems Programmes.
   - The Board of Studies members have gone through the curriculum of 3rd to 8th Semesters of B.E. Electronics and Communication Engineering and 1st to 4th Semesters of M.E. Communication System Programmes.
   - Total number of credits for B.E. Electronics and Communication Engineering programme should be with in 190 and total number of credits for M.E. Communication Systems programme should be with in 74.

Minutes of Board of Studies (BoS) Meeting

UG- B.E Electronics and Communication Engineering

1st Sem:

1. Language Elective- I should be changed as English-I

2. Differential Calculus is renamed as Engineering Mathematics-I

3. Basic of Electrical and Electronics engineering should be renamed as Basic of Electrical and Instrumentation. The Unit consists of

1) AC Machines

2) Transformers

3) DC Machines

4) Sensing Techniques

5) Basic Instrumentation Concepts

2nd Sem:

1. Language Elective –II is renamed as English-II

2. Solid State physics subject is renamed as semiconductor Physics and Devices.

3. Basic Civil and Mechanical Engineering is renamed as Basic of Civil and Mechanical Engineering.

4. In semiconductor devices syllabus, one unit must consist fabrication of semiconductor devices. The text book should follow "Milman Halkias".

5. Cloud Computing and Python Program & Dot Net can be considered as open elective as one credit course.

6. Otherwise online courses may be suggested with certification for 4 weeks to 8 weeks.

- This can be helpful for placement and mapping.

- Select Courses and foundation program according to the department.

7. Add Text book to all the subjects. It must cover 4 units and it is to the level of prentice hall of India.

8. In addition to the Text book, the provision of NPTEL link, tutorial link and open course of NPTEL details also included.

9. The content of English I & English II also include conversation and communicative level.

10. As per the AICTE norms, total credit is 176 as minimum. We can also adopt the same thing.

11. While framing of Syllabus, keeping in mind of syllabus level as topper, middle level and slow level. In future may provide choice of selecting different electives for toppers.

3rd Sem:

1. Transforms & partial differential equation is renamed as partial differential equation & Linear Algebra.

2. Data Structures using C++ is renamed as Data structures and C++.

3. Analog integrated Circuits subject is removed and electronic circuits are added.

4. Electronic Circuits 5th unit has contain semiconductor devices such as Thyristers, Triac, Diac, SCR and Opto couplers.

5. Digital Electronics Lab and Electronic Circuits Lab incorporated.

6. Technical Seminar- I is moved to V semester.

4th Sem:

1. Linear algebra and Numerical methods subject is replaced by Probability & random Process.

2. Numerical methods content may be included in Engineering mathematics-I & Engineering mathematics-II.

3. Remove Technical seminar –II from this semester.

4. Analog communication subject is moved to Vth semester.

5. Microcontroller and interfacing subject is removed and Microprocessor and Microcontroller is added.

6. Analog Integrated Circuits theory and lab is included in this semester.

5th Sem:

1. Digital Communication techniques is moved to VI semester and analog communication is added.

2. Principles of Digital signal processing is renamed as Digital Signal Processing.

3. Computer Network lab is removed and analog communication lab is added.

4. Mini Project –I is removed and technical seminar is added.

6th Sem:

1. Fibre optic communication is moved to 7th semester.

2. Digital Communication subject is added.

3. RF and Microwave Engineering is removed and Wireless Networks and Standards is added.

4. Engineering Proficiency Program may be considered as company courses or it may be merged with main courses. It may be Extra credit course and helpful for placement.

5. Students third review of the projects may be evaluated by industry experts.

7th Sem:

1. Engineering Economics is renamed as Industrial Economics.

2. Digital Image Processing and Embedded systems is moved to elective list.

3. Microwave Electronics and Wireless communication subjects are added in this semester.

4. Optical Communication Lab is renamed as Microwave and Optical Lab.

5. Mini Project –I is added.

8th Sem:

1. Various electives papers were added in the elective list like Foundation Skills in integrated Product development, Cloud computing, virtual instrumentation etc.,

PG - M.E Communication Systems

1st Sem:

1. Mathematics for communication engineers is renamed as Advanced Engineering Mathematics.

2. Statistical Signal Processing is renamed as Advanced Signal Processing.

3. Advanced Radiation System is removed and High Speed Communication Networks included.

4. Advanced Modulation and Coding technique is renamed as Advanced Digital Communication.

5. Communicative English –I is renamed as Business English-I.

2nd Sem:

1. Research methodology subject is removed and FPGA based communication system design is included.

2. Advanced Wireless networks is replaced by Information theory and Coding.

3. RF lab is renamed as RF system design laboratory.

4. Communicative English-II is renamed as Business English-II

3rd Sem:

1. Various new Electives were included based on new technologies.

4th Sem:

1.Project work

Question Paper Patterns

UG Programme.
Part A – 10 x 1 = 10 Marks
Part B – 10 x 2 = 20 Marks
Part C – 5 x 14 = 70 Marks (Either or)
Total = 100 Marks, Duration = 3 Hours

Evaluation Methods

Theory	**Practical**
Continuous Assessment = 40%	Continuous Assessment = 50%
Semester Examinations = 60%	Semester Examinations = 50%

5. The following resolutions were passed

- It is resolved to recommend the curriculum and syllabi of first and second semester of B.E Electronics and Communication Engineering and M.E Communication Systems Programmes to the Academic Council for approval.
- It is resolved to recommend the curriculum of 3rd – 8th Semester B.E Electronics and Communication Engineering and 3rd and 4th semester M.E Communication system Programmes to the academic Council for approval.
- It is resolved to recommend the question paper pattern UG/PG Programmes and evaluation methodology for approval.

The Chairman, Board of Studies thanked all the members for their valuable suggestions and coordination.

Date : 8.7.2017

Place : Nagapattinam

8/7/17

Chairman – BoS (ECE)
CHAIRMAN
Board of Studies
Department of Electronic & Communication Engineering
EGS Pillay Engineering College (Autonomous)
Nagapattinam.

E.G.S. PILLAY ENGINEERING COLLEGE

(An Autonomous Institution, Affiliated to Anna University, Chennai)
Nagore Post, Nagapattinam – 611 002, Tamilnadu.

Department of Electronics and Communication Engineering

Minutes of Board of Studies (BoS) Meeting

UG-B.E Electronics and Communication Engineering

Name of the Department	Electronics and Communication Engineering
Name of the Programmes	B.E. Electronics and Communication Engineering
Meeting No.	02
Date & Time	21.04.2018 & 10.00 A.M.
Venue	ECE - SJB 115

1. Chairman / BoS welcomed all members for the 2nd meeting of Board of Studies.
2. Chairman / BoS introduced the members.
3. Chairman / BoS review the contents of 1st BOS meeting.
4. Chairman / BoS briefed the members about the Agenda.
   - To finalize the curriculum and syllabi of 3rd – 8th Semester B.E ECE.
   - Any other matters.
5. The following are the suggestions given by the members of Board of Studies in curriculum.
   - Elective IX is changed as open elective .
   - Open elective is changed to elective IX.
   - Elective X is introduced instead of embedded systems.
   - Self-study elective is named as elective X.

3rd Sem:

1. In engineering mathematics-III, 2 units must consist of linear algebra and 3 units must consist of partial differential equations, unit 2 and 4 is removed from engineering mathematics-III.
2. Syllabus to be updated in Data Structures and C++ and latest content to be included.
3. In network analysis and synthesis, 3 units must consist of Analysis part and 2 units must consist of Synthesis part and unit five to be removed, add text book to Network analysis and synthesis.
4. In digital circuits and system, number systems must to be added in 1st unit as 1st topic, introduction about Verilog to be included in 5th unit and modify the syllabus, memory can be reduced in 5th unit and it is moved to VLSI or microcontroller, Design of Verilog program is moved to 5th unit, 5th unit title is renamed as Programmable Logic Devices and HDL programming and text book to be added for Verilog programming.
5. In Electronics Circuits, Text book 5 and 6 replaced as 1 and 2.
6. In Digital Electronics lab, subdivisions and IC number to be removed.
7. Remove references for Laboratory courses, utilize the theory references for laboratory also and make uniform number of experiments for all laboratory courses as 12.
8. In Electronics Circuits lab, Frequency Response of CE/CB/CS amplifier is renamed as design and analysis of CE and CB amplifier.

21/4/18

21/04/18
(Dr. J. Ramesh)

Sem 4:

1. Syllabus to be updated for Probability and Random Process.
2. In Signals and Systems, unit 3 must start with impulse response – Convolution Integral - Analysis and characterization of LTI system using Fourier and Laplace transform, in unit 5, Analysis and characterization of DT system using Z transform is changed to Analysis and characterization of DT system using DTFT and Z transform, references will be unique for all subjects and remove the program concepts in all units.
3. In Analog Integrated Circuits, remove the word tuned amplifier in unit 5 title; Text book to be added, unit 1 must consist of basic Op-Amp characteristics, unit 2 must consist of applications of Op-Amp, unit 3 must consist of Op-Amp based feedback amplifiers and oscillators, unit 4 consist of multivibrator, ADC and DAC, unit 5 consist of application IC's like 555 voltage regulator, switched capacitor, frequency to voltage converter, voltage frequency converter, isolation amplifier, audio and video power amplifier, VCO and PLL.
4. In Microprocessor and Microcontroller, unit 5 title is renamed as Architecture of Advanced Processors.
5. In Transmission Lines and Waveguides, unit 2 title is renamed as Impedance matching in Transmission Lines, text book to be added.
6. In Control Systems Engineering, controllers like PI, PID to be replaced in unit 5 and text book to be added.
7. In Analog Integrated Circuits Laboratory, maximum experiments can be 12 and 2 or 3 Simulation experiments is enough.
8. In Microprocessor and Microcontroller laboratory, 8085 consist of basic arithmetic and logic operations, sorting and searching and floating point, 8051 consist of basic arithmetic and logic operations and 5 interfacing experiments.
9. ~~Computer Architecture will be included as compulsory elective in semester V.~~

Sem 5:

1. In Analog Communication, at the end of the unit 1, superhetrodyne receiver must be added, receiver parameter such as sensitivity, fidelity must be added in unit 2, unit 3 title is changed to Random process [random variables, random process, auto correlation process, power spectral density, stationary process, Wiener–Khinchin theorem, transmission of random process through LTI system, WSS ergodic process, Gaussians] and in unit 4 title is changed to Noise in Communication system [Noise calculation, noise figure, noise temperature, noise equivalent bandwidth, narrowband noise, noise in AM receiver, noise in DSBSC receiver, Noise in SSB receiver, Noise in FM receiver, capture and threshold effect, pre-emphasis and de-emphasis in FM system, Comparison of noise performance of AM and FM systems. In unit 5, PAM, PWM, PPM, Time Division Multiplexing, PFM are to be added. Reference books are replaced by Proakis, Simon Haykin, Digital and Data communication by Jack Quinn.
2. In Digital Signal Processing, unit 1 and unit 5 are swapped and rearrange the unit numbers, reference book 4 need to be changed to reference book 3.
3. In Analog Communication lab, experiment number 1 is changed as generation and demodulation of AM, experiment number 2 is changed as generation and demodulation of FM. Remove experiments number 3 and 4. Experiment number 3 must be FM demodulation using PLL. Experiment number 4 must be study of kit based PAM, PWM. PDM. Experiment number 5 is to be study of FDM and TDM. Experiment number 6 is to be Mat lab/Scilab/Equivalent simulation [Experiment number 6 AM, Experiment number 7 FM, Experiment number 8 super heterodyne, Experiment number 9 is performance analysis of noise in communication system, Experiment number 10 is noise removal(Suppression of interference)
4. In Digital Signal Processing Laboratory, first 7 experiments must be in simulation; next 4 experiments must be in processor. Instead of study experiment, filter implementation using DSPs must be included. Experiment number 9 is generation of waveform using DSPs. Experiment

21/4/18 21/04/18

number 6 should be removed. In experiment number 11, change 5x to 54. Processor name should not be mentioned. In additional experiment, don't mention Texas.

5. In Robotic vision, in unit 1 introduction, 2D image transform, image filtering must be included. Unit 2 and unit 3 must be combined as unit 2. Unit 3 is image recognition [future extraction, transform based, sift, image classification, Bayes classification, SVM, deep learning etc. must be included. Unit 4 title is Video Analytics [Video surveillance, four ground extraction, pedestrian deduction, video analytics for navigation, abounded objects deduction etc. Unit 5 should be related to machine learning. Image processing by Gonzalez, Machine learning by Bishop and pattern recognition by Dudo and hart must be added as reference books.
6. Computer organization and architecture is renamed as Computer Architecture and Organization. Memory related content removed from digital circuits may be brought here in unit 3. Reference book Morris mono should be included.
7. In measurement and Instrumentation, latest book must be added and check the edition. Refer Shahane as a text book.
8. In Virtual instrumentation, in unit 5, description should be added and tools set to be elaborated.

Sem 6:

1. In VLSI Design, unit 1 must be removed, instead of that CMOS digital circuits, COMS invertors, etc. should be included in unit 1.
2. In Digital Communication, unit 1 syllabus should be replaced by university department communication theory 1st unit. Syllabus for unit 2 to unit 5 should be replaced by university department Digital Communication and Techniques. Reference books are also taken from university department.
3. In wireless network and standards, take Schiller and Garg as a text book. It should be removed from further reading.
4. In VLSI Design laboratory, number of experiment should be reduced to 10. Experiment numbers 1, 2 and 3 are combined and consider together as experiment number 1. Similarly Experiment numbers 5, 6 and 7 are combined and consider together as experiment number 3.
5. Information Theory is changed as Information Theory and Coding and follow the University department syllabus.
6. Neural Networks should be removed and move Soft Computing from elective IV (Open Elective) to elective III.

Sem 7:

1. Microwave Electronics is renamed as Microwave Engineering.
2. In wireless communication, unit 4 title is renamed as modulation schemes and spread spectrum. Unit 5 title is renamed as diversities techniques. Few contents from unit 4 is moved to unit 5. Reference book to be added in order.
3. Embedded system is combined with embedded lab. Unit 5 title is renamed as Embedded controller.
4. Embedded System laboratory is to be removed.
5. Pattern recognition, Speech processing, VLSI signal processing, RF system design, Multimedia Communication and Cloud Computing syllabus are to re-written.
6. Remove Advanced Wireless Communication from elective VII.
7. Remove Wireless Ad-Hoc and Sensor Network from elective VII and Ad-Hoc wireless Network or Wireless Sensor Network may be included.

Elective courses:

1. CMOS VLSI design is elective IX to be titled as Digital System Design and Testing. Contents of earlier VLSI design (Core) to be used in Digital System Design and Testing.
2. In RF MEMS, unit 1 is renamed as Introduction, remove & symbol from unit 4.
3. In Digital Switching System is retitled as Digital Switching and Transmission.
4. In ARM Processor, unit 3 may be compressed.
5. In Mobile Computing, reference 2 should be moved to reference 1.

21/4/18

21/04/18
(Dr. J. Ramesh)

6. Mixed Signal IC Design is renamed as Mixed Signal CMOS Design and reference book to be added.
7. In Radar and Navigation Aids unit 5 title should be changed.
8. In Satellite communication reference book to be added.
9. In system on chip syllabus to be rewritten
10. In low power VLSI syllabus to be based on book Gary Yeap
11. In Analog IC design syllabus to be fully changed based university departments.
12. Remove System design with FPGA
13. Remove cryptography from self-study
14. Remove introduction to Embedded controller from self-study elective
15. In ASIC design remove the unit 4 content and add timing and power consumption issues.
16. Broad band mobile cellular communication has to be renamed as Broadband communication network.
17. In VLSI for Wireless communication reference need to be changed.
18. New one credit course(Embedded system design using MSP430, Software Defined Radio, Application of ANN and Fuzzy logic, Real data acquisition and signal processing using LABVIEW, Internet of Things, Micrwave/RF circuit design including 3D EM modeling) replaced instead of Back end design of digital circuits, Android application development, 3D animations AC/DC drives, Raspberry PI
19. In 1 credit paper instead of ANN neural networks and fuzzy logic to be included.
20. Remove SDR from title Software defined Radio
21. Remove IOT from 1 credit course. Instead of that add Python Programming

21/4/18

21/04/18
(Dr. T. Ramesh)

Detail minutes attached.

Chairman (BoS) 21/4/18
RS Koteeshwari

Subject Expert 21/04/18
(Dr. J. Ramesh)

Senior Faculty 21/04/18
(M. VIJAYAKUMAR)

University Nominee 21/4/18
Mala John

Industry Expert

Senior Faculty 21/4/18
(Dr. A. SUNDAR RAJ)

Subject Expert 21/4/20
Dr. M. BHASKAR, NIT
PROF/ECE TRICHY

Alumni 21/4/18
DHAKSHIVAMOORTHY. B
Sch. Architect, TATA comm LTD, CHN

Senior Faculty
(Dr. A. Rajaveen)

Annexure – I

E.G.S. PILLAY ENGINEERING COLLEGE

(An Autonomous Institution, Affiliated to Anna University, Chennai)
Nagore Post, Nagapattinam – 611 002, Tamilnadu.

Board of Studies Meeting

Attendance

Name of the Department : Electronics and Communication Engineering

Meeting No : 02

Date & Time : 21-04-2018 & 10.00 A.M.

Venue : ECE A/C Seminar Hall

Sl.No.	Name	Category	Signature
1	Prof. R.S.KOTEESHWARI	Chairman	21/4/18.
2	Dr. MALA JOHN	University Nominee	21/4/18
3	Dr. M. BHASKAR	External Expert Members	21/4/2018
4	Dr. J. RAMESH		21/04/18
5	Dr. N. B. BALAMURUGAN		
6	Mr. YATHEESH MOHAN	Industry / Corporate Sector	21/4/18
7	Mr. J. MUTHUKUMAR	Alumnus	
8	Mr. B. DHAKSHINAMOORTHY		21/4/18
9	Dr. A. RAJARAM	Internal Members	
10	Dr. A. SUNDAR RAJ		21/4/18
11	Prof. M. VIJAYAKUMAR		21/04/18
12	Prof. V. ELAVARASI		21/4/18

Chairman – BoS (ECE)

CHAIRMAN
Board of Studies
Department of Electronic & Communication Engineering
EGS Pillay Engineering College (Autonomous)
Nagapattinam.

Annexure – I

E.G.S. PILLAY ENGINEERING COLLEGE

(An Autonomous Institution, Affiliated to Anna University, Chennai)
Nagore Post, Nagapattinam – 611 002, Tamilnadu.

Board of Studies Meeting

Attendance

Name of the Department : Electronics and Communication Engineering

Meeting No : 02

Date & Time : 21-04-2018 & 10.00 A.M.

Venue : ECE A/C Seminar Hall

Sl.No.	Name	Category	Signature
1	Prof. R.S.KOTEESHWARI	Chairman	21/4/18
2	Dr. MALA JOHN	University Nominee	21/4/18
3	Dr. M. BHASKAR	External Expert Members	21/4/2018
4	Dr. J. RAMESH		21/04/18
5	Dr. N. B. BALAMURUGAN		
6	Mr. YATHEESH MOHAN	Industry / Corporate Sector	21/4/18
7	Mr. J. MUTHUKUMAR	Alumnus	
8	Mr. B. DHAKSHINAMOORTHY		21/4/18
9	Dr. A. RAJARAM	Internal Members	
10	Dr. A. SUNDAR RAJ		21/4/18
11	Prof. M. VIJAYAKUMAR		21/04/18
12	Prof. V. ELAVARASI		21/4/18

Chairman – BoS (ECE)
CHAIRMAN
Board of Studies
Department of Electronic & Communication Engineering
EGS Pillay Engineering College (Autonomou
Nagapattinam.

E.G.S. PILLAY ENGINEERING COLLEGE

(An Autonomous Institution, Affiliated to Anna University, Chennai)
Nagore Post, Nagapattinam – 611 002, Tamilnadu.

Department of Electronics and Communication Engineering

Minutes of Board of Studies(BoS) Meeting

UG-B.E Electronics and Communication Engineering

Name of the Department	Electronics and Communication Engineering
Name of the Programmes	B.E. Electronics and Communication Engineering
Meeting No.	02
Date & Time	21.04.2018 & 10.00 A.M.
Venue	ECE - SJB 115

1. Chairman/BoS welcomed all members for the 2nd meeting of Board of Studies.
2. Chairman/BoS introduced the members.
3. Chairman/BoS review the contents of 1st BOS meeting.
4. Chairman/BoS briefed the members about the Agenda.
   - To finalize the curriculum and syllabi of 3rd – 8th Semester B.E ECE.
   - Any other matters.
5. The following are the suggestions given by the members of Board of Studies in curriculum.
   - Elective IX is changed as open elective .
   - Open elective is changed to elective IX.
   - Elective X is introduced instead of embedded systems.
   - Self-study elective is named as elective X.

3rd Sem:

1. In engineering mathematics-III, 2 units must consist of linear algebra and 3 units must consist of partial differential equations, unit 2 and 4 is removed from engineering mathematics-III.
2. Syllabus to be updated in Data Structures and C++ and latest content to be included.
3. In network analysis and synthesis, 3 units must consist of Analysis part and 2 units must consist of Synthesis part and unit five to be removed, add text book to Network analysis and synthesis.
4. In digital circuits and system, number systems must to be added in 1st unit as 1st topic, introduction about Verilog to be included in 5th unit and modify the syllabus, memory can be reduced in 5th unit and it is moved to VLSI or microcontroller, Design of Verilog program is moved to 5th unit, 5th unit title is renamed as Programmable Logic Devices and HDL programming and text book to be added for Verilog programming.
5. In Electronics Circuits, Text book 5 and 6 replaced as 1 and 2.
6. In Digital Electronics lab, subdivisions and IC number to be removed.
7. Remove references for Laboratory courses, utilize the theory references for laboratory also and make uniform number of experiments for all laboratory courses as 12.
8. In Electronics Circuits lab, Frequency Response of CE/CB/CS amplifier is renamed as design and analysis of CE and CB amplifier.

Sem 4:

1. Syllabus to be updated for Probability and Random Process.
2. In Signals and Systems, unit 3 must start with impulse response – Convolution Integral - Analysis and characterization of LTI system using Fourier and Laplace transform, in unit 5, Analysis and characterization of DT system using Z transform is changed to Analysis and characterization of DT system using DTFT and Z transform, referenceswill be unique for all subjects and remove the program concepts in all units.
3. In Analog Integrated Circuits, remove the word tuned amplifier in unit 5 title, Text book to be added, unit 1 must consist of basic Op-Amp characteristics, unit 2 must consist of applications of Op-Amp, unit 3 must consist of Op-Amp based feedback amplifiers and oscillators, unit 4 consist of multivibrator, ADC and DAC, unit 5 consist of application IC's like 555 voltage regulator, switched capacitor, frequency to voltage converter, voltage frequency converter, isolation amplifier, audio and video power amplifier, VCO and PLL.
4. In Microprocessor and Microcontroller, unit 5 title is renamed as Architecture of Advanced Processors.
5. In Transmission Lines and Waveguides, unit 2 title is renamed as Impedance matching in Transmission Lines, text book to be added.
6. In Control Systems Engineering, controllers like PI, PID to be replaced in unit 5and text book to be added.
7. In Analog Integrated Circuits Laboratory, maximum experiments can be 12 and 2 or 3 Simulation experiments is enough.
8. In Microprocessor and Microcontroller laboratory, 8085 consist of basic arithmetic and logic operations, sorting and searching and floating point, 8051 consist of basic arithmetic and logic operations and 5 interfacing experiments.
9. Computer Architecture will be included as compulsory elective in semester V.

Sem 5:

1. In Analog Communication, at the end of the unit 1, superhetrodyne receiver must be added, receiver parameter such as sensitivity, fidelity must be added in unit 2, unit 3 title is changed to Random process [random variables, random process, auto correlation process, power spectral density, stationary process, Wiener–Khinchin theorem, transmission of random process through LTI system, WSS ergodic process, Gaussians] and in unit 4 title is changed to Noise in Communication system [Noise calculation, noise figure, noise temperature, noise equivalent bandwidth, narrowband noise, noise in AM receiver, noise in DSBSC receiver, Noise in SSB receiver, Noise in FM receiver, capture and threshold effect, pre-emphasis and de-emphasis in FM system, Comparison of noise performance of AM and FM systems. In unit 5, PAM, PWM, PPM, Time Division Multiplexing, PFM are to be added. Reference books are replaced by Proakis, Simon Haykin, Digital and Data communication by Jack Quinn.
2. In Digital Signal Processing, unit 1 and unit 5 are swapped and rearrange the unit numbers, reference book 4 need to be changed to reference book 3.
3. In Analog Communication lab, experiment number 1 is changed as generation and demodulation of AM, experiment number 2 is changed as generation and demodulation of FM. Remove experiments number 3 and 4. Experiment number 3 must be FM demodulation using PLL. Experiment number 4 must be study of kit based PAM, PWM. PDM. Experiment number 5 is to be study of FDM and TDM. Experiment number 6 is to be Mat lab/Scilab/Equivalent simulation [Experiment number 6 AM, Experiment number 7 FM, Experiment number 8 super heterodyne, Experiment number 9 is performance analysis of noise in communication system, Experiment number 10 is noise removal(Suppression of interference)
4. In Digital Signal Processing Laboratory, first 7 experiments must be in simulation; next 4 experiments must be in processor. Instead of study experiment, filter implementation using DSPs must be included. Experiment number 9 is generation of waveform using DSPs. Experiment

number 6 should be removed. In experiment number 11, change 5x to 54. Processor name should not be mentioned. In additional experiment, don't mention Texas.

5. In Robotic vision, in unit 1 introduction, 2D image transform, image filtering must be included. Unit 2 and unit 3 must be combined as unit 2. Unit 3 is image recognition [future extraction, transform based, sift, image classification, Bayes classification, SVM, deep learning etc. must be included. Unit 4 title is Video Analytics [Video surveillance, four ground extraction, pedestrian deduction, video analytics for navigation, abounded objects deduction etc. Unit 5 should be related to machine learning.Image processing by Gonzalez, Machine learning by Bishop and pattern recognition by Dudo and hart must be added as reference books.
6. Computer organization and architecture is renamed as Computer Architecture and Organization. Memory related content removed from digital circuits may be brought here in unit 3. Reference book Morris mono should be included.
7. In measurement and Instrumentation, latest book must be added and check the edition. Refer Shahane as a text book.
8. In Virtual instrumentation, in unit 5, description should be added and tools set to be elaborated.

Sem 6:

1. In VLSI Design, unit 1 must be removed, instead of that CMOS digital circuits, COMS invertors, etc. should be included in unit 1.
2. In Digital Communication, unit 1 syllabus should be replaced by university department communication theory 1st unit.Syllabus for unit 2 to unit 5 should be replaced by university department Digital Communication and Techniques. Reference books are also taken from university department.
3. In wireless network and standards, take Schiller and Garg as a text book. It should be removed from further reading.
4. In VLSI Design laboratory, number of experiment should be reduced to 10. Experiment numbers 1, 2 and 3 are combined and consider together as experiment number 1. Similarly Experiment numbers 5, 6 and 7 are combined and consider together as experiment number 3.
5. Information Theory is changed as Information Theory and Coding and follow the University department syllabus.
6. Neural Networks should be removed and move Soft Computing from elective IV (Open Elective) to elective III.

Sem 7:

1. Microwave Electronics is renamed as Microwave Engineering.
2. In wireless communication, unit 4 title is renamed as modulation schemes and spread spectrum. Unit 5 title is renamed as diversities techniques. Few contents from unit 4 is moved to unit 5. Reference book to be added in order.
3. Embedded system is combined with embedded lab. Unit 5 title is renamed as Embedded controller.
4. Embedded System laboratory is to be removed.
5. Pattern recognition, Speech processing, VLSI signal processing, RF system design, Multimedia Communication and Cloud Computing syllabus are to re-written.
6. Remove Advanced Wireless Communication from Elective VII.
7. Remove Wireless Ad-Hoc and Sensor Network from Elective VII and Ad-Hoc wireless Network or Wireless Sensor Network may be included.

Elective courses:

1. CMOS VLSI design is elective IX to be titled as Digital System Design and Testing. Contents of earlier VLSI design (Core) to be used in Digital System Design and Testing.
2. In RF MEMS, unit 1 is renamed as Introduction, remove & symbol from unit 4.
3. In Digital Switching System is retitled as Digital Switching and Transmission.
4. In ARM Processor, unit 3 may be compressed.
5. In Mobile Computing, reference 2 should be moved to reference 1.

6. Mixed Signal IC Design is renamed as Mixed Signal CMOS Design and reference book to be added.
7. In Radar and Navigation Aids unit 5 title should be changed.
8. In Satellite communication reference book to be added.
9. In system on chip syllabus to be rewritten
10. In low power VLSI syllabus to be based on book Gary Yeap
11. In Analog IC design syllabus to be fully changed based university departments.
12. Remove System design with FPGA
13. Remove cryptography from self-study
14. Remove introduction to Embedded controller from self-study elective
15. In ASIC design remove the unit 4 content and add timing and power consumption issues.
16. Broad band mobile cellular communication has to be renamed as Broadband communication network.
17. In VLSI for Wireless communication reference need to be changed.
18. New one credit course(Embedded system design using MSP430, Software Defined Radio, Application of ANN and Fuzzy logic, Real data acquisition and signal processing using LABVIEW, Internet of Things, Micrwave/RF circuit design including 3D EM modeling) replaced instead of Back end design of digital circuits, Android application development, 3D animations AC/DC drives, Raspberry PI
19. In 1 credit paper instead of ANN neural networks and fuzzy logic to be included.
20. Remove SDR from title Software defined Radio
21. Remove IOT from 1 credit course. Instead of that add Python Programming

Annexure – I

E.G.S. PILLAY ENGINEERING COLLEGE

(An Autonomous Institution, Affiliated to Anna University, Chennai)
Nagore Post, Nagapattinam – 611 002, Tamilnadu.

Board of Studies Meeting

Attendance

Name of the Department : Electronics and Communication Engineering

Meeting No : —

Date & Time : 15-05-2019 & 10.00 A.M.

Venue : ECE A/C Seminar Hall

Sl.No.	Name	Category	Signature
1	Dr. B. PADMANABAN	Chairman	15/5/19
2	Dr. MALA JOHN	University Nominee	15/5/19
3	Dr. D.SRIRAMKUMAR	External Expert Members	15/5/2019
4	Dr. R. RAMESH		15/5/19.
5	Mr. V. UDAYA SANKAR	Industry / Corporate Sector	
6	Ms. R. NANDHINI	Alumnus	
7	Dr. A. RAJARAM	Internal Members	
8	Dr. A. SUNDARRAJ		
9	Prof. K. PARTHASARATHY		
10	Prof. RS.KOTEESHWARI		
11	Prof. S. CHITRA		
12	Prof. V. ELAVARASI		
13	Prof. M. NUTHUL SRINIVASAN		15/5/19.

Chairman – BoS (ECE)

UG-B.E Electronics and Communication Engineering

BOS Meeting Minutes:

1. Analog and Digital Communication is divided into two courses.
2. Microwave Engineering has to be included.
3. Semester wise subject details have to be changed.
4. Data Structure theory and lab course have to be combined as single integrated course with 3 credits (2 credits for theory and 1 credit for lab).
5. Credit for Electromagnetic theory has to be changed from 4 to 3.
6. Signals and Systems have to be changed to core course.
7. All lab hours have to be changed from 3 hours to 2 hours (1 credit).
8. The name project viva has to be changed as Project work.
9. Professional core elective is renamed as Professional elective.
10. For mini projects, don't mention any specific title or subject.
11. One credit courses such as Verbal, Reasoning, Aptitude etc. have to be removed.
12. Remove the virtual lab or move to lower semester (3 or 4).
13. Comprehensive viva has to be changed as seminar as one credit course.
14. Course code has to be changed as per AICTE format.
15. Combine Microwave theory and lab courses as single course with 4 credits.
16. Elective courses have to be changed as given below
17. In Elective V, combine Fiber optic communication theory and lab as a single integrated course with 3 credits.

Elective – I:

i. Operating Systems
ii. Bio Medical
iii. Embedded Systems
iv. Measurements and Instrumentation

CHAIRMAN
Board of Studies
Department of Electronic & Communication Engineering
EGS Pillay Engineering College (Autonomous)
Nagapattinam.

Elective – 2:

i. Image Processing
ii. Digital Switching and Transmission
iii. Real Time Operating System
iv. Artificial Intelligence

Elective – 3:

i. Computer Architecture
ii. MEMS
iii. Soft Computing
iv. VLSI Signal Processing

Elective – 4:

i. Wireless Sensor Networks
ii. RADAR and Navigation
iii. Mixed Signal CMOS Design
iv. Internet of Things

Elective – 5:

i. Fiber Optic Communication
ii. Satellite Communication
iii. Network Security
iv. Web Technology

HOD / ECE

Dr. B. PADMANABAN, M.E., Ph.D.,
Professor & Head,
Department of Electronics and
Communication Engineering,
E.G.S. Pillay Engineering College,
Nagapattinam - 611 002.

E.G.S. PILLAY ENGINEERING COLLEGE
(An Autonomous Institution, Affiliated to Anna University, Chennai)
Nagore Post, Nagapattinam – 611 002, Tamilnadu.

Department of Electronics and Communication Engineering

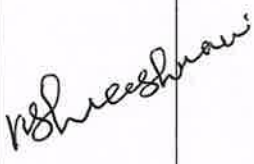
Minutes of the Board of Studies (BoS) held on 15.05.2019

The UG Curriculum(Regulations 2019) pertaining to B.E. – ECE has been framed (Enclosed in Annexure – I)

Sl.No.	Name	Category	Signature
1	**Dr.B.Padmanaban,** Professor & Head, Department of Electronics and Communication Engineering, E.G.S.Pillay Engineering College,Nagapattinam – 611002. Email.ID:dr.padmanaban83@gmail.com Mobile Number:9994163733	**Chairman**	15/5/19
2	**Dr.Mala John,** Professor, Department of Electronics and Communication Engineering, Madras Institute of Technology, MIT Road, Radha Nagar, Chromepet, Chennai – 600044 Email ID:malajohn@annauniv.edu Mob. No.9444443706	**University Nominee**	15/5/19
3	**Dr.D.Sriramkumar,** Professor, Department of Electronics and Communication Engineering, National Institute of technology,Tiruchirappalli – 620015. Email.ID: srk@nitt.edu Mobile Number :9443494495	**External Expert Members**	15/5/2019
4	**Dr.R.Ramesh,** Senior Assistant Professor, School of Electrical and Electronics Engineering, SASTRA University, Thanjavur. Email ID:ramesh@ece.sastra.edu Mobile Number : 9944062307		15/5/19

Department of Electronics and Communication Engineering

Board of Studies (BoS) Members

Sl.No.	Name	Category	Signature
6	**R.Nandhini,** Teaching Faculty, Chinmaya vidyalya(CBSE), Nagapattinm **Mobile:8438136248**	**Alumini**	
7	**Dr.A.Rajaram** Professor, Department of Electronics and Communication Engineering, E.G.S.Pillay Engineering College,Nagapattinam – 611002. Mobile: 9942035947	**Internal Members**	
8	**Dr.A.SundarRaj** Associate Professor, Department of Electronics and Communication Engineering, E.G.S.Pillay Engineering College,Nagapattinam – 611002. Email ID: sundarraj198886@gmail.com Mobile:9865173406	**Internal Members**	
9	**K.Parthasarathy,** Associate Professor , Department of Electronics and Communication Engineering, E.G.S.Pillay Engineering College,Nagapattinam – 611002. Email ID: kp_sarathi78@yahoo.co.in Mobile:984294464		
10	**R.S.Koteeshwari,** Associate Professor , Department of Electronics and Communication Engineering, E.G.S.Pillay Engineering College,Nagapattinam – 611002. Email ID: eshwari.kote5@gmail.com Mobile:7904338947		
11	**S.Chitra,** Asst.Professor, Department of Electronics and Communication Engineering, E.G.S.Pillay Engineering College,Nagapattinam – 611002. Email ID: chithrapriyadharshini@gmail.com Mobile:9487485874		
12	**V.Elavarasi** Asst.Professor,		

	E.G.S.Pillay Engineering College,Nagapattinam – 611002. Email ID: chithrapriyadharshini@gmail.com Mobile:9487485874		
12	**V.Elavarasi** Asst.Professor, Department of Electronics and Communication Engineering, E.G.S.Pillay Engineering College,Nagapattinam – 611002. Email ID: suthanmathi@gmail.com Mobile:9943397484		
13	**M.Nuthal Srinivasan,** Asst. Professor, Department of Electronics and Communication Engineering, E.G.S.Pillay Engineering College,Nagapattinam – 611002. Email ID:nuthal4u@gmail.com Mobile:9597077499		25/5/19

Annexure – I
Dated: 15.05.2019

Degree: B.E. **Programme: ECE** **Batch: 2019-2020 onwards**

Credit Distribution

S.No	Category	Credits
A	**Foundation Courses**	**57**
	Humanities, Social Science and management (HSS)	7
	Basic Science (BS)	27
	Engineering Science (ES)	23
B	Professional Core Courses	**53**
C	**Elective Courses**	**30**
	Programme specific Electives	15
	Open Electives	6
	Humanities, Social Science and management (HSS) Electives	9
D	Project work, seminar, internship in industry or at Higher Learning institutions	**10**
E	Mandatory Courses – Environment Science, Induction Programme, Indian Constitution, Essence of Indian Tradition knowledge	**Non Credit (Not included for CGPA)**
	Minimum Credits to be earned for the award of the Degree	**150 (from A to E) and the successful completion of Mandatory Courses**

A. FOUNDATION COURSES: **Total Credits to be earned: (50)**

a. Humanities and Social Science (7)

S.No.	Course Code	Name of the Course	Number of Hours / Week			Credit
			L	T	P	
		THEORY				
1.		English for Engineers	2	-	-	2
2.		Professional Ethics	2	-	-	2
3.		Communication skills	-	-	2	1
4.		Presentation skills	-	-	2	1
		PRACTICALS				
1.		English Laboratory	-	-	2	1

b. Humanities, Social Science and Management Electives(9)

S.No.	Course Code	Name of the Course	Number of Hours / Week			Credit
			L	T	P	
		Elective 1				
1.		Project Management and Finance	3	-	-	3

CHAIRMAN
Board of Studies
Department of Electronic & Communication Engineering
EGS Pillay Engineering College (Autonomous)
Nagapattinam.

1.		BEEE lab	0	0	2	1
2.		Computer Programming Lab	0	0	2	1
3.		Computer Hardware trouble shooting Lab	0	0	2	1
4.		CAD for Engineering Graphics Lab	0	0	2	1

B. CORE COURSES

Credits to be earned: (53)

Sl. No.	Course Code	Name of the Course	Number of Hours / Week			Credit
			L	T	P	
		THEORY				
1.		Digital Electronics	3	0	0	3
2.		Electronics Circuits	3	0	0	3
3.		Signals and Systems	3	1	0	4
4.		Analog Integrated Circuits	3	0	0	3
5.		Microprocessors and Microcontrollers	3	0	0	3
6.		Communication Systems - I	3	1	0	4
7.		Communication Systems - II	3	0	0	3
8.		Transmission Lines and Wave Guides	3	0	0	3
9.		Digital Signal Processing	3	0	0	3
10.		Communication Networks	3	0	0	3
11.		VLSI Design	3	0	0	3
12.		Antenna and Wave Propagation	3	0	0	3
13.		Wireless Communication	3	0	0	3
14.		Microwave and Fiber Optic Communication	3	0	2	4
		PRACTICALS				
15.		Digital Electronics Laboratory	0	0	2	1
16.		Electronic Circuits Laboratory	0	0	2	1
17.		Analog Integrated Circuits and Simulation Laboratory	0	0	3	1
18.		Microprocessors and Microcontrollers Laboratory	0	0	3	1
19.		Communication Systems Lab	0	0	2	1
20.		Digital Signal Processing Laboratory	0	0	3	1
21.		VLSI Design Laboratory	0	0	3	1
22.		Communication and Networks Laboratory	0	0	3	1

C. PROFESSIONAL ELECTIVES

Credits to be earned: (15)

Sl. No.	Course	Name of the Course	Number of	Credit

CHAIRMAN
Board of Studies
Department of Electronic & Communication Engineering
EGS Pillay Engineering College (Autonomous)
Nagapattinam.

Semester I (First year]
Branch/Course Electronics & Communication Engineering

Sr.No.	Course Code	CourseTitle	L	T	P	Contact Hrs./wk.	Credits
1		Engineering Mathematics-I	3	1	0	4	4
2		Applied Chemistry	3	0	0	3	3
3		Programming for Problem solving	3	0	0	3	3
4		English for Engineers	2	0	0	2	2
5		Chemistry Lab	0	0	2	2	1
6		Computer Programming Lab	0	0	2	2	1
7		English Lab	0	0	2	2	1
8		Communication Skills	1	0	0	1	1
						TOTALCREDITS	16

Semester II (First year]
Branch/Course Electronics & Communication Engineering

Sr.No.	Course Code	CourseTitle	L	T	P	Contact Hrs./wk.	Credits
1		Engineering Mathematics – II	3	1	0	4	4
2		Applied Physics	3	0	0	3	3
3		Basic Electrical and Electronics Engineering	3	0	0	3	3
4		Engineering Graphics	3	0	0	3	3
5		Computer Hardware Trouble shooting Lab	0	0	2	2	1
6		Physics Lab	0	0	2	2	1
7		BEEE Lab	0	0	2	2	1
8		CAD Lab	0	0	2	2	1
9		Presentation Skills	0	0	2	2	1
						TOTALCREDITS	18

Semester III (Second year]
Branch/Course Electronics & Communication Engineering

Sr.No.	Course Code	CourseTitle	L	T	P	Contact Hrs./wk.	Credits
1		Engineering Mathematics – III	3	1	0	4	4
2		Digital Electronics	3	0	0	3	3
3		Electronics Circuits	3	0	0	3	3
4		Signals & Systems	3	1	0	4	4
5		Biology for Engineers	3	0	0	3	3
6		Digital Electronics Lab	0	0	2	2	1
7		Electronics Circuits Lab	0	0	2	2	1
	MC	Constitution of India					0

CHAIRMAN
Board of Studies
Department of Electronic & Communication Engineering
EGS Pillay Engineering College (Autonomous)
Nagapattinam.

		TOTALCREDITS	19

Semester IV (Second year]
Branch/Course Electronics & Communication Engineering

Sr.No.	Course Code	CourseTitle	L	T	P	Contact Hrs./wk.	Credits
1		Electromagnetic Fields	3	1	0	4	4
2		Analog Integrated Circuits	3	0	0	3	3
3		Communication Systems – I	3	1	0	4	4
4		Microprocessor and Microcontroller	3	0	0	3	3
5		Probability and Random Process	4	0	0	4	4
6		Analog Integrated Circuits and Simulation Lab	0	0	2	2	1
		Microprocessor and Microcontroller Lab	0	0	2	2	1
7	MC	Environmental Science					0
						TOTALCREDITS	20

Semester V (Third year]
Branch/Course Electronics & Communication Engineering

Sr.No.	Course Code	CourseTitle	L	T	P	Contact Hrs./wk.	Credits
1		Communication Systems - II	3	0	0	3	3
2		Control Systems	3	0	0	3	3
3		Data Structures and C++	2	0	2	4	3
4		Digital Signal Processing	3	0	0	3	3
5		Transmission Lines and Waveguides	3	0	0	3	3
6		Professional Elective – I	3	0	0	3	3
7		Communication System Lab	0	0	2	2	1
8		Digital Signal Processing Lab	0	0	2	2	1
9		Mini Project 1	0	0	2	2	1
	MC	Essence of Indian Traditional Knowledge					0
						TOTALCREDITS	21

Semester VI (Third year]
Branch/Course Electronics & Communication Engineering

Sr.No.	Course Code	CourseTitle	L	T	P	Contact hrs./wk.	Credits
1		VLSI Design	3	0	0	3	3
2		Communication Networks	3	0	0	3	3
3		Antenna and Wave Propagation	3	0	0	3	3
4		Professional Elective – II	3	0	0	3	3
5		HSS Elective – I	3	0	0	3	3
6		Open Elective – I	3	0	0	3	3

CHAIRMAN
Board of Studies
Department of Electronic & Communication Engineering
EGS Pillay Engineering College (Autonomous)
Nagapattinam.

meeting and EG

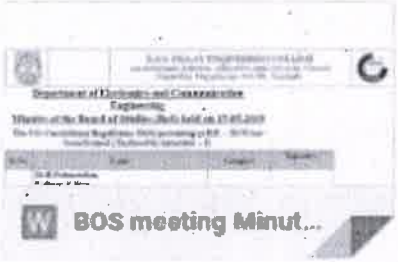
https://mail.google.com/mail/u/0/#search/malajohnmit%40gmail.com/KtbxLwGvWCmHKWpCBXbhWVlrhvhsGstmgV

Gmail malajohnmit@gmail.com

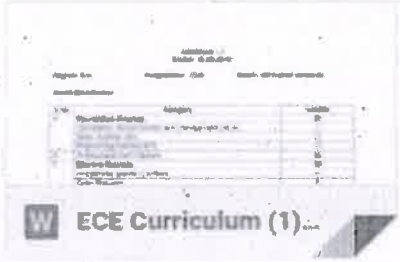
1 of 3

Mobile:+91-9994163733.

2 Attachments



BOS meeting Minut...



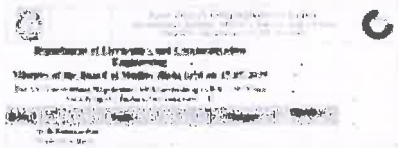
ECE Curriculum (1)...

HOD ECE <hodece@egspec.org>

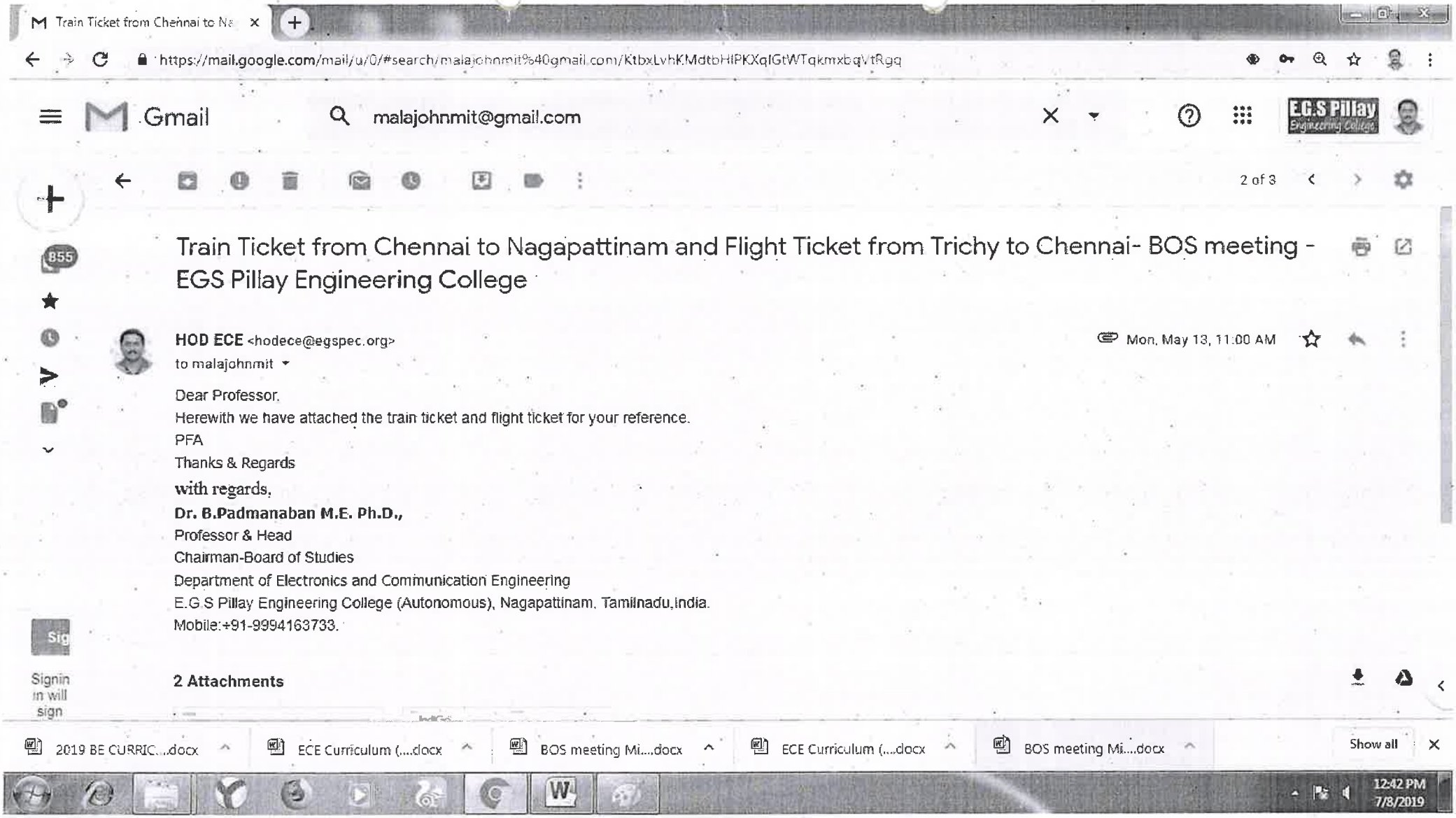
Fri, May 17, 12:21 PM

to principal

2 Attachments



CHAIRMAN
Board of Studies
Department of Electronic & Communication Engineering
EGS Pillay Engineering College (Autonomous)
Nagapattinam.



CHAIRMAN
Board of Studies
Department of Electronic & Communication Engineering
EGS Pillay Engineering College (Autonomous)
Nagapattinam.

E.G.S. PILLAY ENGINEERING COLLEGE (Autonomous)
Nagapattinam - 611 002, Tamilnadu, India.
Provisionally Accredited by NBA (CSE, EEE & Mech) / Accredited by NAAC with 'A' Grade
Affiliated to Anna University, Chennai / Approved by AICTE, New Delhi
An ISO 9001 : 2015 Certified Institution
Website: www.egspec.org, Email: principal@egspec.org, Ph: (04365) 251112, Fax: (04365) 251114

Dr. S. RAMABALAN, M.E., Ph.D.,
Principal

Ref. No. : EGSPEC/BoS/2018-2019/16

Date : 09/05/2019

To

Dr.R.Ramesh,

SAP,

School of Electrical and Electronics Engineering,

SASTRA University,

Thanjavur.

Sir,

Sub: Convening the Board of Studies Meeting - Invitation - Reg.

We are honoured to have your good self as an External Expert in the Board of Studies for the Department of **Electronics and Communication Engineering** of our Institution for the academic year 2018-19.

Considering the academic schedule of the esteemed members, it is planned to convene the Meeting of the Board of Studies on **Wednesday, the 15th of May 2019 at 10.00 a.m.** in our College to consider and approve the Curriculum & Syllabus prepared for the Regulation R2019 based on the AICTE model curriculum for UG Degree Courses in Engineering & Technology (Jan 2018).

We invite you to attend the meeting and share your valuable experience for the successful implementation of the Regulation R2019 in our Autonomous system for the benefit of all stake holders.

Thanking you,

Yours faithfully,

E.G.S. PILLAY ENGINEERING COLLEGE NAGAPATTINAM-611 002

PRINCIPAL

E.G.S. PILLAY ENGINEERING COLLEGE (Autonomous)
Nagapattinam - 611 002, Tamilnadu, India.
Provisionally Accredited by NBA (CSE, EEE & Mech) / Accredited by NAAC with 'A' Grade
Affiliated to Anna University, Chennai / Approved by AICTE, New Delhi
An ISO 9001 : 2015 Certified Institution
Website: www.egspec.org, Email: principal@egspec.org, Ph: (04365) 251112, Fax: (04365) 251114

Dr. S. RAMABALAN, M.E., Ph.D.,
Principal

Ref. No. : EGSPEC/BoS/2018-2019/18

Date : 09/05/2019

To

Mr.B.Dhakshinamoorthy,

Solution Architect,

Tata Communication Limited,

4th floor, Jayanth Tech Park,

Nungambakkam,

Chennai – 600 089.

Sir,

Sub: Convening the Board of Studies Meeting - Invitation - Reg.

We are honoured to have your good self as Alumni in the Board of Studies for the Department of **Electronics and Communication Engineering** of our Institution for the academic year 2018-19.

Considering the academic schedule of the esteemed members, it is planned to convene the Meeting of the Board of Studies on **Wednesday, the 15th of May 2019 at 10.00 a.m.** in our College to consider and approve the Curriculum & Syllabus prepared for the Regulation R2019 based on the AICTE model curriculum for UG Degree Courses in Engineering & Technology (Jan 2018).

We invite you to attend the meeting and share your valuable experience for the successful implementation of the Regulation R2019 in our Autonomous system for the benefit of all stake holders.

Thanking you,

Yours faithfully,

PRINCIPAL

E.G.S. PILLAY ENGINEERING COLLEGE (Autonomous)
Nagapattinam - 611 002, Tamilnadu, India.
Provisionally Accredited by NBA (CSE, EEE & Mech) / Accredited by NAAC with 'A' Grade
Affiliated to Anna University, Chennai / Approved by AICTE, New Delhi
An ISO 9001 : 2015 Certified Institution
Website: www.egspec.org, Email: principal@egspec.org, Ph: (04365) 251112, Fax: (04365) 251114

Dr. S. RAMABALAN, M.E., Ph.D.,
Principal

Ref. No. : EGSPEC/BoS/2018-2019/15

Date : 09/05/2019

To

Dr.Mala John,

Professor,

Department of Electronics and Communication Engineering,

Madras Institute of Technology,

MIT Road, Radha Nagar,

Chromepet, Chennai – 600 044.

Madam,

Sub: Convening the Board of Studies Meeting - Invitation - Reg.

We are honoured to have your good self as University Nominee of Anna University, Chennai in the Board of Studies for the Department of **Electronics and Communication Engineering** of our Institution for the academic year 2018-19.

Considering the academic schedule of the esteemed members, it is planned to convene the Meeting of the Board of Studies on **Wednesday, the 15th of May 2019 at 10.00 a.m.** in our College to consider and approve the Curriculum & Syllabus prepared for the Regulation R2019 based on the AICTE model curriculum for UG Degree Courses in Engineering & Technology (Jan 2018).

We invite you to attend the meeting and share your valuable experience for the successful implementation of the Regulation R2019 in our Autonomous system for the benefit of all stake holders.

Thanking you,

Yours faithfully,

E.G.S. PILLAY ENGINEERING COLLEGE NAGAPATTINAM-611 002

PRINCIPAL

EGSPEC

E.G.S. PILLAY ENGINEERING COLLEGE (Autonomous)

Nagapattinam - 611 002, Tamilnadu, India.
Provisionally Accredited by NBA (CSE, EEE & Mech) / Accredited by NAAC with 'A' Grade
Affiliated to Anna University, Chennai / Approved by AICTE, New Delhi
An ISO 9001 : 2015 Certified Institution
Website: www.egspec.org, Email: principal@egspec.org, Ph: (04365) 251112, Fax: (04365) 251114

Dr. S. RAMABALAN, M.E., Ph.D.,
Principal

Ref. No. : EGSPEC/BoS/2018-2019/17

Date : 09/05/2019

To

Dr.D.Sriramkumar,

Professor,

Department of Electronics and Communication Engineering,

National Institute of Technology,

Tirichirappalli – 620 015.

Sir,

Sub: Convening the Board of Studies Meeting - Invitation - Reg.

We are honoured to have your good self as an External Expert in the Board of Studies for the Department of **Electronics and Communication Engineering** of our Institution for the academic year 2018-19.

Considering the academic schedule of the esteemed members, it is planned to convene the Meeting of the Board of Studies on **Wednesday, the 15th of May 2019 at 10.00 a.m.** in our College to consider and approve the Curriculum & Syllabus prepared for the Regulation R2019 based on the AICTE model curriculum for UG Degree Courses in Engineering & Technology (Jan 2018).

We invite you to attend the meeting and share your valuable experience for the successful implementation of the Regulation R2019 in our Autonomous system for the benefit of all stake holders.

Thanking you,

Yours faithfully,

E.G.S. PILLAY ENGINEERING COLLEGE NAGAPATTINAM-611 002

PRINCIPAL

E.G.S. PILLAY ENGINEERING COLLEGE
(An Autonomous Institution, Affiliated to Anna University, Chennai)
Nagore Post, Nagapattinam – 611 002, Tamil Nadu.

Minutes of Board of Studies (BoS) Meeting

Name of the Department	Electronics and Communication Engineering
Name of the Programmes	B.E. Electronics and Communication Engineering M.E. Communication Systems
Meeting No.	03 (04)
Date & Time	12.06.2020 & 03.00 P.M.
Venue	Online Mode - Google Meets Room

1. Chairman / BoS welcomed all members for the 3rd meeting of Board of Studies.
2. Chairman / BoS introduced the members.
3. Chairman / BoS review the contents of 2nd BOS meeting.
4. Chairman / BoS briefed the members about the Agenda.
   - To finalize the curriculum and syllabi of 3rd & 4th Semester in B.E - Electronics and Communication Engineering and 1st & 2nd Semester in M. E - Communication Systems.
   - Any other matters.
5. The following are the suggestions given by the members of Board of Studies in curriculum.

- Open elective: 5 subjects per each module should include.
- 6th semester needs one programming paper if possible, in python or Java.
- Suggested to include Programming concepts in Internet of Thinks, Soft Computing and Embedded Systems papers.
- Python and Java is very much needed in current scenario (software development methodology) so it should be included in syllabus.

B.E Electronics and Communication Engineering

III - Semester:

1. Electron devices -> Electronic devices: Reduce content of unit 4, and in 3rd unit, FINFET and advance concepts should be removed. Update reference books to be latest editions.
2. Circuit and Networks: Remove Dr.Salivahanan book, include Dr.Arumugam book.
3. Digital Electronics: Change 5th unit to applications or 3rd unit can be split and it is added in 5th unit. Dr.Salivahanan need to be included, Goanker book to be excluded.
4. Biology for Engineers: Reduce the content of 4th and 5th units.
5. Object oriented programming and Data Structure: Can be updated in python.
6. Electron Devices and Circuits Laboratory-> Devices and Circuits laboratory, Mention IC name in experiments, Include DIAC in experiment, change title of experiments as it is in Digital Electronics lab.
7. Digital Electronics Laboratory: Xilinx simulation is good.
8. Object oriented programming and Data Structure Laboratory: Can include python based experiments.

CHAIRMAN
Board of Studies
Department of Electronic & Communication Engineering
EGS Pillay Engineering College (Autonomous)

IV - Semester:

1. Electronic circuits: Unit 2: Multi stage differential amplifiers should be removed. Include Negative feedback amplifiers and reduce applications in unit 5. Telescope and Cascade amplifier to be removed.
2. Signals and systems: Unit 3 title to continuous time signal than CT. Include latest reference books. Entire syllabus to be revised. Heavy portion.
3. Micro controller and its application: 8085 should be included from Digital electronics and name should be changed as microprocessor and micro controller. 2-unit microprocessor +3-unit micro controllers. Interfaces may be added in syllabus.
4. Electromagnetic fields: syllabus need to be reduced and re-modify it.
5. Analog Integrated Circuits and its application->Analog Integrated Circuits: Linear noise in amplifiers should be removed. Include some basic concepts.
6. Laboratory Course
7. Electronic Circuits Lab: Re-modify experiment 1,2,3 and include Tuned amplifier, Feedback amplifier, Power amplifier concepts. More number of simulation experiments needed.
8. Analog Integrated Circuits Laboratory: DC changes to Regulated Power Supply (RPS)
9. Micro controller and its application Laboratory->Microprocessor and Micro controller lab.

V - Semester:

1. Analog and Digital Communication may be split into Analog communication and Digital Communication
2. Computer Organization can be moved into Elective. Instead IOT or Soft computing can be included

General Comments:

1. As already two subjects on English are available, it is better include the content of the subject "Verbal ability" in the subject "Communication skills" in the second semester and replace with other technical subject.
2. The subject on "Constitution of India" may be included as included as the audit compulsory course instead of credit course.
3. It is good to see the subject "Biology for Engineers" in the curriculum.
4. The mentioning of "Artificial Neural Network and Genetic algorithms" in the Further reading section for the subject: Object oriented programming is inappropriate.

M.E. Communication Systems

I - Semester:

1. Applied Engineering Mathematics should contain Random process and Probability subject concepts.
2. Optical switching networks ->Optical networks.
3. Wireless Communication Engineering: Include 5G concepts, Remove repeated UG contents.

II - Semester:

1. Technical seminar may be split into Technical seminar 1 and Technical seminar II.
2. FPGA based communication system design can be renamed as Digital integrated circuits based communication system design.

CHAIRMAN
Board of Studies
Department of Electronic & Communication Engineering
EGS Pillay Engineering College (Autonomous)

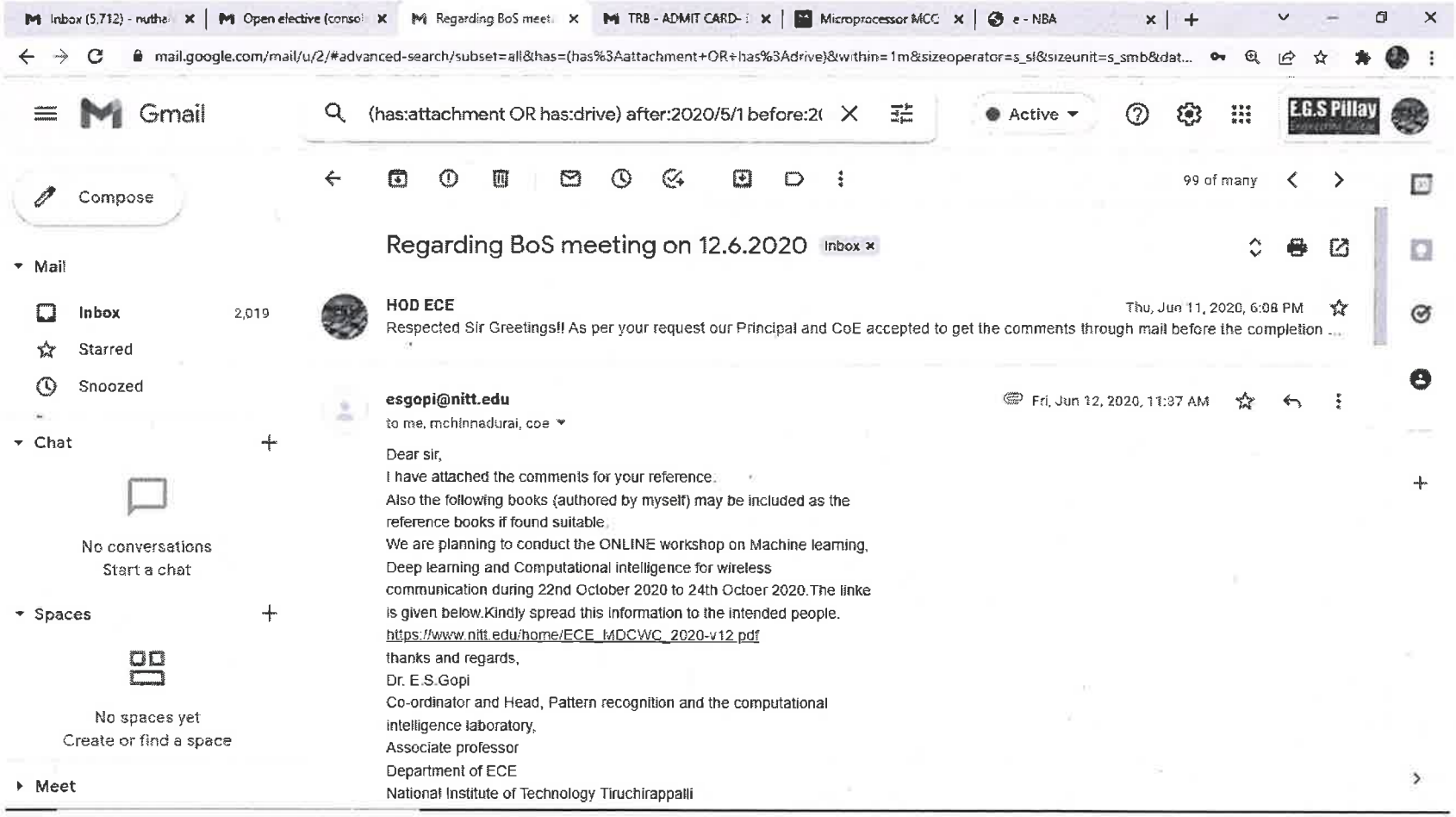
General Comments:

1. Choose some other subject for FPGA Based Communication System Design and Electromagnetic Interference & Compatibility in System Design. Those can replace with RF IC design, Multimedia compression techniques, Radiating systems or Digital Integrated circuits.
2. Remove Optical Signal Processing.

Chairman – BOS / ECE

CHAIRMAN
Board of Studies
Department of Electronic & Communication Engineering
EGS Pillay Engineering College (Autonomous)
Nagapattinam.

Screen Shots – BOS 2020



CHAIRMAN
Board of Studies
Department of Electronic & Communication Engineering
EGS Pillay Engineering College (Autonomous)
Nagapattinam.

E.G.S. PILLAY ENGINEERING COLLEGE
(An Autonomous Institution, Affiliated to Anna University, Chennai)
Nagore Post, Nagapattinam – 611 002, Tamil Nadu.

Academic Year 2019 - 2020 **Date: 12.6.2020**

Department of ECE

Action Taken for BOS Meeting Minutes

B.E - ELECTRONICS AND COMMUNICATION ENGINEERING

SEMESTER III

1. Electron devices changed to Electronic devices and Electron Devices and Circuits Laboratory changed to Devices and circuits laboratory
2. Reference books are updated for Circuit and Networks, Digital electronics, Electron devices. And some advance topic as removed as per meeting decision.
3. In DigitalElectronics, 5th unit is completely changed and new syllabus created.
4. Object oriented programming and Data Structure is requested to include python language along with C++ implementation

SEMESTER IV

1. Analog Integrated Circuits and Its application is changed to Analog Integrated Circuits and Micro Controller and its applications changed to Microprocessor and Micro controllers.
2. For Microprocessor and Micro controllers and its laboratory course new syllabus is created
3. Some topics are changed to as per meeting decisions in Electronic Circuits, Electronic circuit laboratory, Electromagnetic fields

M.E - COMMUNICATION SYSTEMS

1. Applied Engineering Mathematics included with Random process and probability subject concepts
2. Optical Switching networks changed to optical networks and syllabus created as per decisions in meeting
3. Wireless communication Networks syllabus modified as per discussion in meeting.

CHAIRMAN
Board of Studies
Department of Electronic & Communication Engineering
EGS pillay Engineering College (Autonomous)
Nagapattinam.

E.G.S. PILLAY ENGINEERING COLLEGE

(An Autonomous Institution, Affiliated to Anna University, Chennai)
Nagore Post, Nagapattinam – 611 002, Tamil Nadu.

Academic Year 2019 - 2020 **Date: 12.6.2020**

Department of ECE

Board of Studies Meeting - Attendance

Sl.No.	Name	Category
1.	**Dr.B.Padmanaban** Professor & Head, Department of Electronics and Communication Engineering, E.G.S.Pillay Engineering College, Nagapattinam – 611002. Email.ID:dr.padmanaban83@gmail.com Mobile Number:9994163733	**Chairman**
2.	**Dr. E.S.Gopi** Associate Professor, Department of Electronics and Communication Engineering, National Institute of Technology, Tiruchirappalli - 620015 E-mail:esgopi@nitt.edu	**University Nominee**
3.	**Dr.N.B.Balamurugan** Associate Professor, Department of Electronics and Communication Engineering, Thiagarajar College of Engineering Thiruparankundram, Madurai 625015 nbbalamurugan@tce.edu	**External Subject Expert Members**
4.	**Dr.R.Ramesh** Professor, School of Electrical and Electronics Engineering, SASTRA University, Thanjavur. Email ID:ramesh@ece.sastra.edu Mobile Number : 9944062307	
5.	**Mr.G.Balaji** Software Arctech, Robort Boseh Ltd Coimbatore Email ID:raviram1645@gmail.com Mobile Number : 9047150446	**Industry / Corporate Sector**
6.	**Mr.R.Sriram** Programmer Analyst, Cognizant Technology Solutions, Chennai Email ID:raviram1645@gmail.com Mobile Number : 9047150446	**Alumni**
7.	**Mr.V.Vijay** Senior Testing Engineer (R&D),	

CHAIRMAN
Board of Studies
Department of Electronic & Communication Engineering
EGS Pillay Engineering College (Autonomous)
Nagapattinam.

Sl.No.	Name	Category
	Akas Medical Equipments Ltd, Chennai Email ID :vvijay.veeramani@gmail.com Mobile Number :9487845449	
8.	**Dr.S.M.Ramesh** Professor, Department of Electronics and Communication Engineering, E.G.S.Pillay Engineering College, Nagapattinam – 611002. Email.ID: hodece@egspec.org Mobile Number: 9443505446	**Internal Members**
9.	**Dr.A.Sundar Raj** Associate Professor, Department of Electronics and Communication Engineering, E.G.S.Pillay Engineering College, Nagapattinam – 611002. Email ID: sundarraj198886@gmail.com Mobile:9865173406	
10.	**Mrs.R.S.Koteeshwari** Associate Professor , Department of Electronics and Communication Engineering, E.G.S.Pillay Engineering College, Nagapattinam – 611002. Email ID: eshwari.kote5@gmail.com Mobile:7904338947	**Internal Members**
11.	**Mrs.S.Chitra** Asst.Professor Department of Electronics and Communication Engineering, E.G.S.Pillay Engineering College, Nagapattinam – 611002. Email ID: chithrapriyadharshini@gmail.com Mobile:9487485874	
12.	**Mrs.V.Elavarasi** Asst.Professor Department of Electronics and Communication Engineering, E.G.S.Pillay Engineering College, Nagapattinam – 611002. Email ID: suthanmathi@gmail.com Mobile:9943397484	**Internal Members**
13.	**Mr.M.Nuthal Srinivasan** Asst. Professor Department of Electronics and Communication Engineering, E.G.S.Pillay Engineering College, Nagapattinam – 611002. Email ID:nuthal4u @gmail.com Mobile:9597077499	
14.	**Mr.D.Devarajan** Asst. Professor	

CHAIRMAN
Board of Studies
Department of Electronic & Communication Engineering
EGS Pillay Engineering College (Autonomous)
Nagapattinam.

Sl.No.	Name	Category
	Department of Electronics and Communication Engineering, E.G.S.Pillay Engineering College, Nagapattinam – 611002. Email ID:devarajan@egspec.org Mobile:9894055342	Internal Members
15.	**Dr. M. Irshad Ahmed** Asst. Professor Department of Electronics and Communication Engineering, E.G.S.Pillay Engineering College, Nagapattinam – 611002. Email ID:devarajan@egspec.org Mobile:9894055342	
16.	**Mr. S. Jim Hawkinson** Asst. Professor Department of Electronics and Communication Engineering, E.G.S.Pillay Engineering College, Nagapattinam – 611002. Email ID: jimhawkinson@egspec.org Mobile: 7358591939	Internal Members
17.	**Mr. S.K. Pragadesh** Asst. Professor Department of Electronics and Communication Engineering, E.G.S.Pillay Engineering College, Nagapattinam – 611002. Email ID: skpragadesh@gmail.com Mobile: 9843356131	

CHAIRMAN
Board of Studies
Department of Electronic &
Communication Engineering
EGS Pillay Engineering College (Autonomous)
Nagapattinam.

E.G.S. PILLAY ENGINEERING COLLEGE

(An Autonomous Institution, Affiliated to Anna University, Chennai)
Nagore Post, Nagapattinam – 611 002, Tamilnadu.

Minutes of Board of Studies (BoS) Meeting

Name of the Department	Electronics and Communication Engineering
Name of the Programmes	B.E. Electronics and Communication Engineering M.E. Communication Systems
Meeting No.	06
Date & Time	09.07.2021 & 03.15 P.M.
Venue	Google Meet (Online)

1. Chairman / BoS welcomed all members for the 6th meeting of Board of Studies.
2. Chairman / BoS introduced the members.
3. Chairman / BoS review the contents of 5th BOS meeting.
4. Chairman / BoS briefed the members about the Agenda.
   - To finalize the curriculum and syllabi of B.E- ECE and M.E.Communication systems.
   - Vision, Mission, PEOs and PSOs are discussed and finalized.
   - Any other matters.
5. The following are the suggestions given by the External expert members of Board of Studies:
   For M.E.Communication System
   - Category of Elective to be included in Curriculum.
   - If Audit course reflected in mark sheet means, two levels of result is required, to know whether they completed or not. Otherwise it is not necessary.
   - Mini project is not mandatory in Second semester. Otherwise, it should be in the level upto preparing literature survey.
   - In Semester 1, Probability stochastic process topic is to be included in Mathematics course.
   - Pattern recognition and VLSI based subject may be included as one of the elective course.
   - In communication system lab, experiments to be done in both mode, i.e. simulation and Hardware.
   - In second semester instead of mini project, laboratory course may be included like Communication system modelling and simulation.

BOS-Chairman (Dr.B.Padmanaban Prof & Head/ECE/ EGSPEC)	**University Nominee (Dr.E.S.Gopi, ASP/ECE NIT-Trichy)**	**Subject Expert 1 (Dr.N.B.Balamurugan ASP/ECE, TCE,Madurai)**	**Subject Expert 2 (Dr.R. Ramesh, Prof-EEE SASTRA,Tanjore)**
Industrial Expert (Mr.S.Ajith selva vigneswaran,Senior engineer,National Technology center for ports waterways ,IIT Madras,Chennai)	**Alumni (Mr. R.Sriram, Cloud Admin Support Accenture, Chennai)**	**Senior Faculty1 (Dr. A. Sundar Raj, ASP/ECE/EGSPEC)**	**Senior Faculty 2 (Dr. D. Devarajan, AP/ECE/EGSPEC)**

E.G.S. PILLAY ENGINEERING COLLEGE

(An Autonomous Institution, Affiliated to Anna University, Chennai)
Nagore Post, Nagapattinam – 611 002, Tamil Nadu.

Academic Year 2020 - 2021Date: 09.07.2021

Department of ECE

Board of StudiesMeeting - Attendance – (Online Mode)

Sl.No.	Name	Category
1.	**Dr.B.Padmanaban** Professor & Head, Department of Electronics and Communication Engineering, E.G.S.Pillay Engineering College, Nagapattinam – 611002. Email.ID: hodece@egspec.org Mobile Number: 9994163733	**Chairman**
2.	**Dr. E.S.Gopi** Associate Professor, Department of Electronics and Communication Engineering, National Institute of Technology, Tiruchirappalli -620015 E-mail:esgopi@nitt.edu	**University Nominee**
3.	**Dr.N.B.Balamurugan** Associate Professor, Department of Electronics and Communication Engineering, Thiagarajar College of Engineering Thiruparankundram, Madurai 625015 nbbalamurugan@tce.edu	**External Subject Expert Members**
4.	**Dr.R.Ramesh** Professor, School of Electrical and Electronics Engineering, SASTRA University, Thanjavur. Email ID:ramesh@ece.sastra.edu Mobile Number : 9944062307	
5.	**Mr.S.AjithSelvaVigneswaran,** Senior engineer, National Technology center for ports waterways, IIT Madras, Chennai. Email ID:ajith@ntcpwc.iitm.ac.in Mobile Number : 8870934327	**Industry / Corporate Sector**
6.	**Mr.R.Sriram** Programmer Analyst, Cognizant Technology Solutions, Chennai Email ID:raviram1645@gmail.com Mobile Number : 9047150446	**Alumni**
7.	**Mr.V.Vijay** Senior Testing Engineer (R&D), Akas Medical Equipments Ltd, Chennai	

CHAIRMAN
Board of Studies
Department of Electronic & Communication Engineering
EGS Pillay Engineering College (Autonomous)
Nagapattinam.

	Email ID :vvijay.veeramani@gmail.com Mobile Number :9487845449	
8.	**Dr.A.Sundar Raj** Associate Professor, Department of Electronics and Communication Engineering, E.G.S.Pillay Engineering College, Nagapattinam – 611002. Email ID: sundarraj198886@gmail.com Mobile:9865173406	**Internal Members**
9.	**Dr. M. Irshad Ahmed** Asst. Professor Department of Electronics and Communication Engineering, E.G.S.Pillay Engineering College, Nagapattinam – 611002. Email ID:devarajan@egspec.org Mobile:9894055342	
10.	**Dr. D.Devarajan** Asst. Professor Department of Electronics and Communication Engineering, E.G.S.Pillay Engineering College, Nagapattinam – 611002. Email ID:devarajan@egspec.org Mobile:9894055342	**Internal Members**
11.	**Mrs.R.S.Koteeshwari** Associate Professor , Department of Electronics and Communication Engineering, E.G.S.Pillay Engineering College, Nagapattinam – 611002. Email ID: eshwari.kote5@gmail.com Mobile:7904338947	
12.	**Mrs. V.Elavarasi** Asst.Professor Department of Electronics and Communication Engineering, E.G.S.Pillay Engineering College, Nagapattinam – 611002. Email ID: suthanmathi@gmail.com Mobile:9943397484	**Internal Members**
13.	**Mr. M.Nuthal Srinivasan** Asst. Professor Department of Electronics and Communication Engineering, E.G.S.Pillay Engineering College, Nagapattinam – 611002. Email ID:nuthal4u @gmail.com Mobile:9597077499	
14.	**Mrs. S.Chitra** Asst.Professor Department of Electronics and Communication Engineering, E.G.S.Pillay Engineering College, Nagapattinam – 611002. Email ID: chithrapriyadharshini@gmail.com Mobile:9487485874	

CHAIRMAN
Board of Studies
Department of Electronic & Communication Engineering
E.G.S.Pillay Engineering College (Autonomous)

15.	**Mr. S. Jim Hawkinson** Asst. Professor Department of Electronics and Communication Engineering, E.G.S.Pillay Engineering College, Nagapattinam – 611002. Email ID:jimhawkinson@egspec.org,Mobile: 7358591939	**Internal Members**
16.	**Mr. S.K. Pragadesh** Asst. Professor Department of Electronics and Communication Engineering, E.G.S.Pillay Engineering College, Nagapattinam – 611002. Email ID: skpragadesh@gmail.com Mobile: 9843356131	

CHAIRMAN
Board of Studies
Department of Electronic & Communication Engineering
EGS Pillay Engineering College (Autonomous)
Nagapattinam.

E.G.S. PILLAY ENGINEERING COLLEGE

(An Autonomous Institution, Affiliated to Anna University, Chennai)
Nagore Post, Nagapattinam – 611 002, Tamil Nadu.

Academic Year 2020 - 2021 **Date: 09.07.2021**

Department of ECE

BOS Meeting – Account Details for BOS External Members

Sl.No.	Name of the Members	Category	Account Details
1.	**Dr. E.S.Gopi** Associate Professor Department of Electronics and Communication Engineering National Institute of Technology Tiruchirappalli - 620015 E-mail:esgopi@nitt.edu	**University Nominee**	Account Number: 30046143771 IFSC Code: SBIN0001617 Bank Name: State Bank of India Branch: NIT, Trichy.
2.	**Dr.N.B.Balamurugan** Associate Professor Department of Electronics and Communication Engineering Thiagarajar College of Engineering Thiruparankundram, Madurai 625015 nbbalamurugan@tce.edu	**External Subject Expert Members**	Account Number: 601301151789 IFSC Code: ICIC0000563 Bank Name: ICICI Bank Branch: TCE, Madurai.
3.	**Dr.R.Ramesh** Associate Professor School of Electrical and Electronics Engineering SASTRA University Thanjavur - 613401 Email ID:ramesh@ece.sastra.edu Mobile Number : 9944062307		Account Number : 500101010926387 IFSC Code: CIUB0000528 Bank Name: City Union Bank Branch: Thirumalaisamudram, Thanjavur
4.	**Mr.S.Ajith Selva Vigneswaran** , Senior engineer, National Technology center for ports waterways, IIT Madras, Chennai.600036 Email ID:ajith@ntcpwc.iitm.ac.in Mobile Number : 8870934327	**Industry / Corporate Sector**	Account Number : 881024886437 IFSC Code: DBSS0IN0811 Bank Name: DBS Bank Branch: Nariman Point,Mumbai
5.	**Mr.R.Sriram** Programmer Analyst Cognizant Technology Solutions Chennai - 600123 Email ID:raviram1645@gmail.com Mobile Number : 9047150446	**Alumni**	Account Number : 916010038503949 IFSC Code: UTIB0001119 Bank Name: Axis Bank Branch: Medavakkam, Chennai.

CHAIRMAN
Board of Studies
Department of Electronic & Communication Engineering
EGS Pillay Engineering College (Autonomous)
Nagapattinam.

M.E. COMMUNICATION SYSTEMS

REGULATION - 2021

I TO IV SEMESTERS CURRICULAM AND SYLLABI

SEMESTER I

SL. NO.	COURSE CODE	COURSE TITLE	CATEGORY	PERIODS PER WEEK			TOTAL CONTACT PERIODS	CREDITS
				L	T	P		
1.		Applied Engineering Mathematics for Communication System	FC	3	1	0	4	4
2.		Advanced Digital Communication	PCC	3	0	0	3	3
3.		Program Elective – I	PEC	3	0	0	3	3
4.		Program Elective – II	PEC	3	0	0	3	3
5.		Research Methodology and IPR	RMC	3	0	0	3	3
6.		Audit Course – I	AC	2	0	0	2	0
PRACTICALS								
7.		Communication System Laboratory	PCC	0	0	4	4	2
8.		Communication Skill Laboratory	EEC	0	0	4	4	2
			TOTAL	**17**	**01**	**08**	**26**	**20**

SEMESTER II

SL. NO.	COURSE CODE	COURSE TITLE	CATEGORY	PERIODS PER WEEK			TOTAL CONTACT PERIODS	CREDITS
				L	T	P		
1.		Wireless Communication Engineering	PCC	3	0	0	3	3
2.		Optical Switching and Networking	PCC	3	0	0	3	3
3.		High Speed Communication Networks	PCC	3	0	0	3	3
4.		Program Elective – III	PEC	3	0	0	3	3
5.		Program Elective – IV	PEC	3	0	0	3	3
6.		Audit Course – II	AC	2	0	0	2	0
PRACTICALS								
7.		Wireless Communication Networks Laboratory	PCC	0	0	4	4	2
8.		Mini Project with Seminar	EEC	0	0	4	4	2
			TOTAL	**17**	**00**	**08**	**25**	**19**

CHAIRMAN
Board of Studies
Department of Electronic & Communication Engineering
EGS Pillay Engineering College (Autonomous)
Nagapattinam

SEMESTER III

SL. NO.	COURSE CODE	COURSE TITLE	CATEGORY	PERIODS PER WEEK			TOTAL CONTACT PERIODS	CREDITS
				L	T	P		
1.		Antenna Design and Analysis	PCC	3	0	0	3	3
2.		Program Elective – V	PEC	3	0	0	3	3
3.		Open Elective	OEC	3	0	0	3	3
PRACTICALS								
4.		RF System Design Laboratory	PCC	0	0	4	4	2
5.		Dissertation – I	EEC	0	0	20	20	10
			TOTAL	**09**	**00**	**24**	**30**	**21**

SEMESTER IV

SL. NO.	COURSE CODE	COURSE TITLE	CATEGORY	PERIODS PER WEEK			TOTAL CONTACT PERIODS	CREDITS
				L	T	P		
PRACTICALS								
1.		Dissertation – II	EEC	0	0	32	32	16
			TOTAL	**00**	**00**	**32**	**32**	**16**

TOTAL CREDITS TO BE EARNED FOR THE AWARD OF THE DEGREE - 76

CHAIRMAN
Board of Studies
Department of Electronic & Communication Engineering
EGS Pillay Engineering College (Autonomous)
Nagapattinam.

PROGRAM CORE COURSES (PCC)

SL. NO.	COURSE CODE	COURSE TITLE	CATEGORY	PERIODS PER WEEK			TOTAL CONTACT PERIODS	CREDITS
				L	T	P		
1		Applied Engineering Mathematics for Communication System	PCC	3	1	0	4	4
2		Advanced Digital Communication	PCC	3	0	0	3	3
3		Communication System Laboratory	PCC	0	0	4	4	2
4		Wireless Communication Engineering	PCC	0	0	4	4	2
5		Optical Switching and Networking	PCC	3	0	0	3	3
6		High Speed Communication Networks	PCC	3	0	0	3	3
7		Mini Project with Seminar	PCC	3	0	0	3	3
8		Antenna Design and Analysis	PCC	0	0	4	4	2

PROGRAM ELECTIVE COURSES (PEC)

SEMESTER I, ELECTIVE – I

SL. NO.	COURSE CODE	COURSE TITLE	CATE GORY	PERIODS PER WEEK			TOTAL CONTAC T PERIODS	CREDITS
				L	T	P		
1.		Electromagnetic Interference and Compatibility in System Design	PEC	3	0	0	3	3
2.		Information Retrieval Techniques	PEC	3	0	0	3	3
3.		Multimedia Compression Techniques	PEC	3	0	0	3	3
4.		Advanced Microwave Communication	PEC	3	0	0	3	3
5.		FPGA Based Communication System Design	PEC	3	0	0	3	3

SEMESTER I, ELECTIVE – II

SL. NO.	COURSE CODE	COURSE TITLE	CATE GORY	PERIODS PER WEEK			TOTAL CONTAC T PERIODS	CREDITS
				L	T	P		
1.		Network Routing Algorithms	PEC	3	0	0	3	3
2.		Biometrics for Network Security	PEC	3	0	0	3	3
3.		Embedded Networking	PEC	3	0	0	3	3

CHAIRMAN
Board of Studies
Department of Electronic & Communication Engineering
EGS Pillay Engineering College (Autonomous)
Nagapattinam.

4.		Advanced Digital Signal Processing	PEC	3	0	0	3	3
5.		Network Engineering and Management	PEC	3	0	0	3	3

SEMESTER II, ELECTIVE – III

SL. NO.	COURSE CODE	COURSE TITLE	CATE GORY	PERIODS PER WEEK			TOTAL CONTACT PERIODS	CREDITS
				L	T	P		
1.		Mobile Adhoc Networks	PEC	3	0	0	3	3
2.		RF System Design for Wireless Communication	PEC	3	0	0	3	3
3.		Artificial Intelligent Techniques	PEC	3	0	0	3	3
4.		Cognitive Radio	PEC	3	0	0	3	3
5.		Radiating Systems	PEC	3	0	0	3	3

SEMESTER II, ELECTIVE – IV

SL. NO.	COURSE CODE	COURSE TITLE	CATE GORY	PERIODS PER WEEK			TOTAL CONTACT PERIODS	CREDITS
				L	T	P		
1.		Speech and Audio Signal Processing	PEC	3	0	0	3	3
2.		Beam forming in Wireless Communication	PEC	3	0	0	3	3
3.		Microwaves and Radar	PEC	3	0	0	3	3
4.		Optical Signal Processing	PEC	3	0	0	3	3
5.		Remote Sensing by Lasers	PEC	3	0	0	3	3

SEMESTER III, ELECTIVE – V

SL. NO.	COURSE CODE	COURSE TITLE	CATE GORY	PERIODS PER WEEK			TOTAL CONTACT PERIODS	CREDITS
				L	T	P		
1.		Network on Chip Design	PEC	3	0	0	3	3
2.		Communication Network Security	PEC	3	0	0	3	3
3.		Machine Learning Algorithms	PEC	3	0	0	3	3
4.		Advanced Display System	PEC	3	0	0	3	3
5.		Medical Image Processing	PEC	3	0	0	3	3

CHAIRMAN
Board of Studies
Department of Electronic & Communication Engineering
EGS Pillay Engineering College (Autonomous)
Nagapattinam.

RESEARCH METHODOLOGY AND IPR COURSES (RMC)

SL. NO.	COURSE CODE	COURSE TITLE	PERIODS PER WEEK			TOTAL CONTACT PERIODS	CREDITS
			L	T	P		
1.		Research Methodology and IPR	3	0	0	3	3

OPEN ELECTIVE COURSES [OEC]

SL. NO.	COURSE CODE	COURSE TITLE	CATE GORY	PERIODS PER WEEK			TOTAL CONTACT PERIODS	CREDITS
				L	T	P		
1.		FPGA Based Communication System Design	OEC	3	0	0	3	3
2.		Data Converters	OEC	3	0	0	3	3
3.		Smart Antennas	OEC	3	0	0	3	3
4.		Advanced Digital Image processing	OEC	3	0	0	3	3
5.		Embedded System Design using MSP430	OEC	3	0	0	3	3
6.		Microwave Remote Sensing	OEC	3	0	0	3	3
7.		High Speed Switching Architecture	OEC	3	0	0	3	3
8.		RFIC Design	OEC	3	0	0	3	3
9.		Space Time Communication	OEC	3	0	0	3	3
10.		Communication Protocol Engineering	OEC	3	0	0	3	3

AUDIT COURSES (AC)

SL. NO	COURSE CODE	COURSE TITLE	PERIODS PER WEEK			CREDITS
			L	T	P	
1.		English for Research Paper Writing	2	0	0	0
2.		Disaster Management	2	0	0	0
3.		Sanskrit for Technical Knowledge	2	0	0	0
4.		Value Education	2	0	0	0
5.		Constitution of India	2	0	0	0
6.		Pedagogy Studies	2	0	0	0
7.		Stress Management by Yoga	2	0	0	0
8.		Personality Development Through Life Enlightenment Skills	2	0	0	0
9.		Unnat Bharat Abhiyan	2	0	0	0

CHAIRMAN
Board of Studies
Department of Electronic & Communication Engineering
EGS Pillay Engineering College (Autonomous)
Nagapattinam.

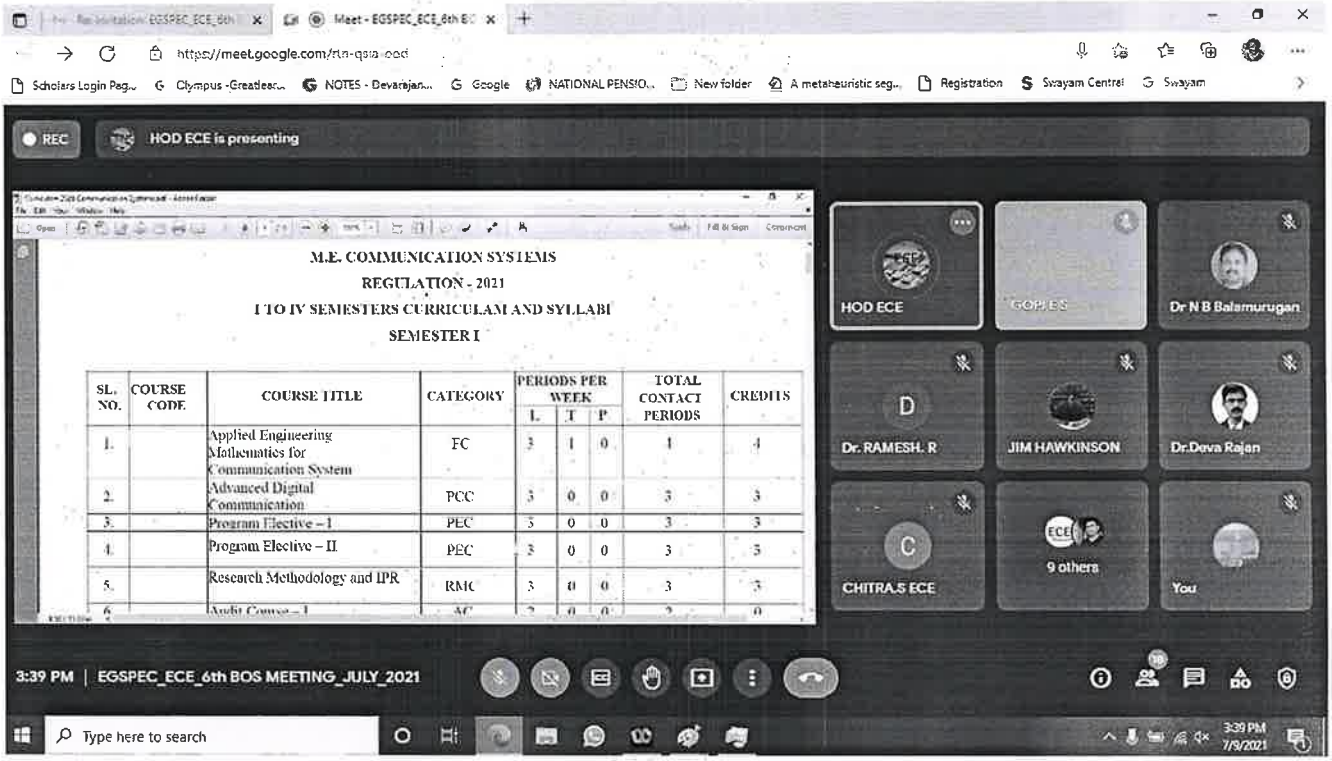
EMPLOYABILITY ENHANCEMENT COURSES (EEC)

SL. No.	Course Code	Course Title	Periods per week			Credits	Semester
			Lecture	Tutorial	Practical		
		Communication Skill Laboratory	0	0	4	2	1
1.		Mini Project with Seminar	0	0	4	2	2
2.		Dissertation - I	0	0	20	10	3
3.		Dissertation - II	0	0	32	16	4

SUMMARY

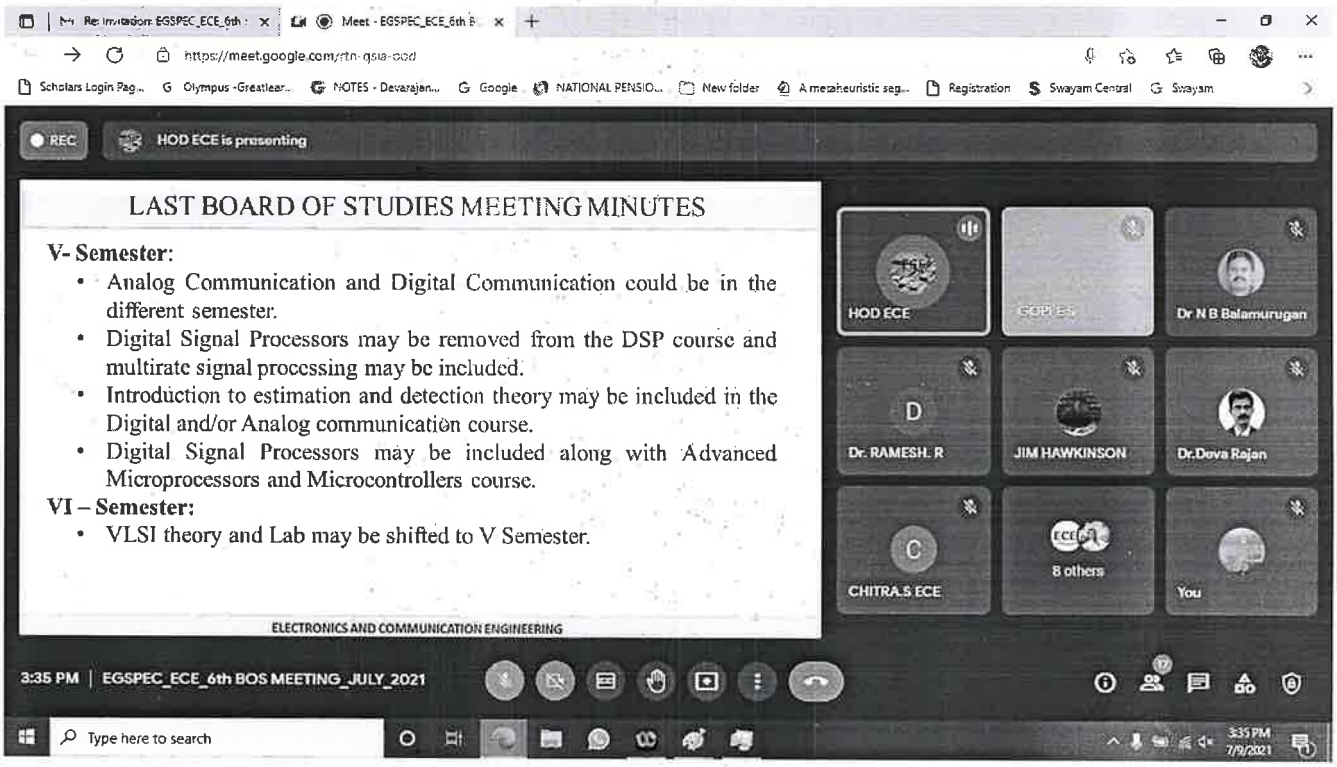
M.E COMMUNICATION SYSTEMS						
	Subject Area	Credits Per Semester				Credits Total
		I	II	III	IV	
1.	FC	04	00	00	00	04
2.	PCC	05	11	05	00	21
3.	PEC	06	06	03	00	15
4.	RMC	03	00	00	00	03
5.	OEC	00	00	03	00	03
6.	EEC	02	02	10	16	30
7.	Non Credit/Audit Courses	✓	✓	00	00	00
	Total Credit	**20**	**19**	**21**	**16**	**76**

CHAIRMAN
Board of Studies
Department of Electronic & Communication Engineering
EGS Pillay Engineering College (Autonomous)
Nagapattinam.

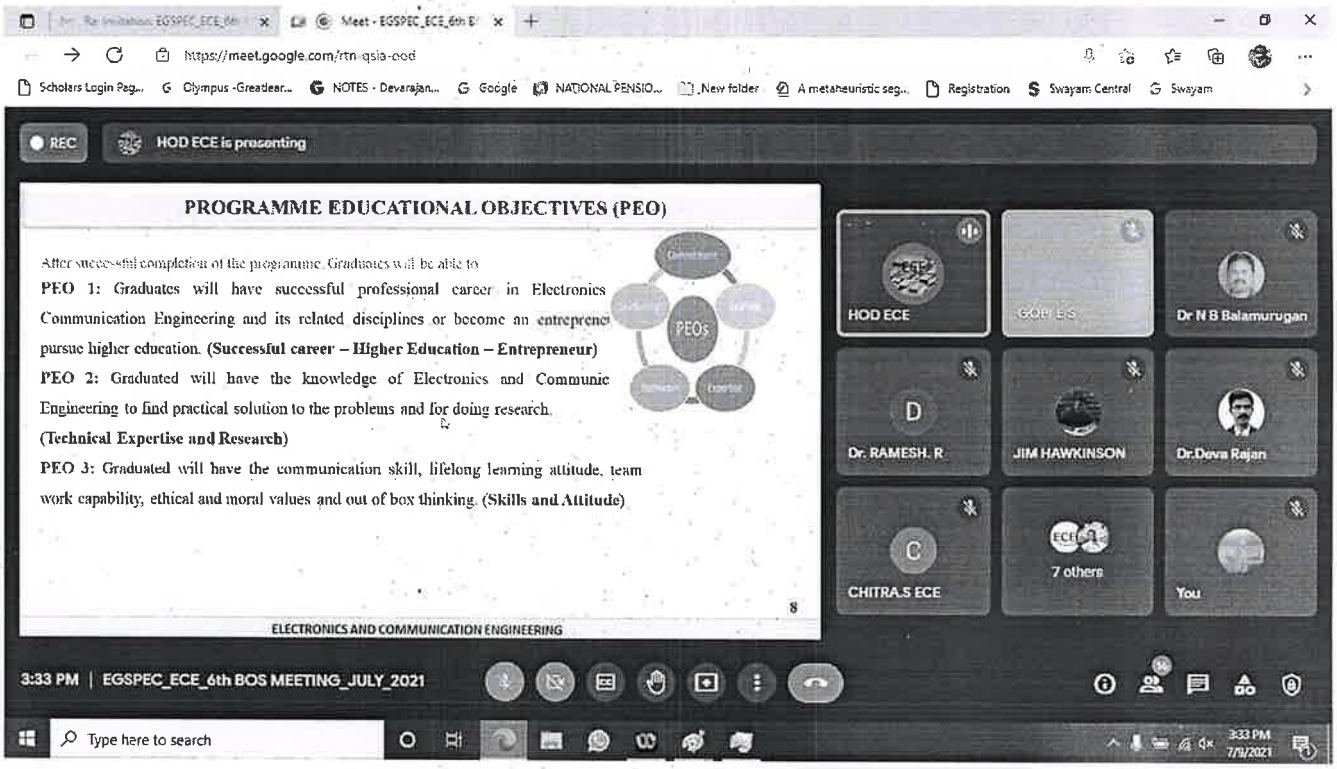


SL. NO.	COURSE CODE	COURSE TITLE	CATEGORY	PERIODS PER WEEK			TOTAL CONTACT PERIODS	CREDITS
				L	T	P		
1.		Applied Engineering Mathematics for Communication System	FC	3	1	0	1	4
2.		Advanced Digital Communication	PCC	3	0	0	3	3
3.		Program Elective – I	PEC	3	0	0	3	3
4.		Program Elective – II	PEC	3	0	0	3	3
5.		Research Methodology and IPR	RMC	3	0	0	3	3
6		Audit Course – I	AC	2	0	0	2	0

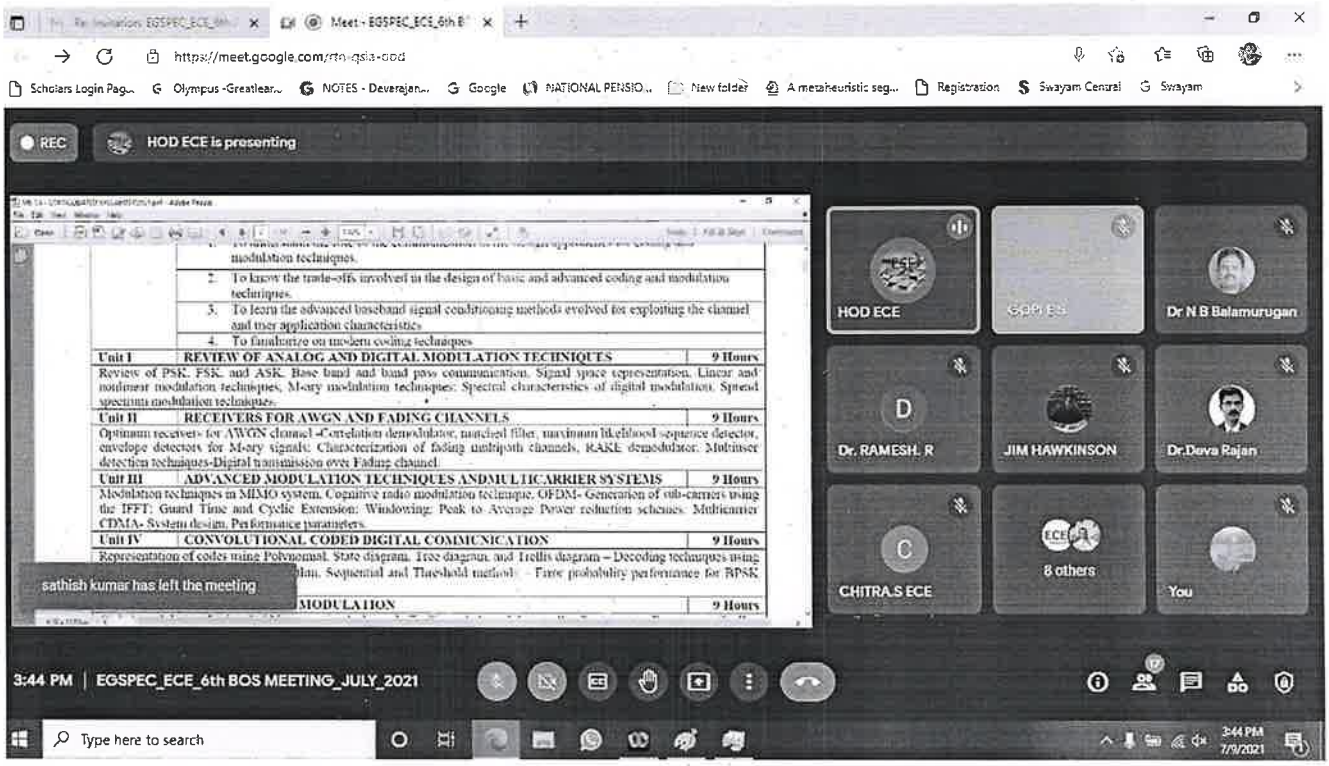
CHAIRMAN
Board of Studies
Department of Electronic & Communication Engineering
EGS Pillay Engineering College (Autonomous)
Nagapattinam.



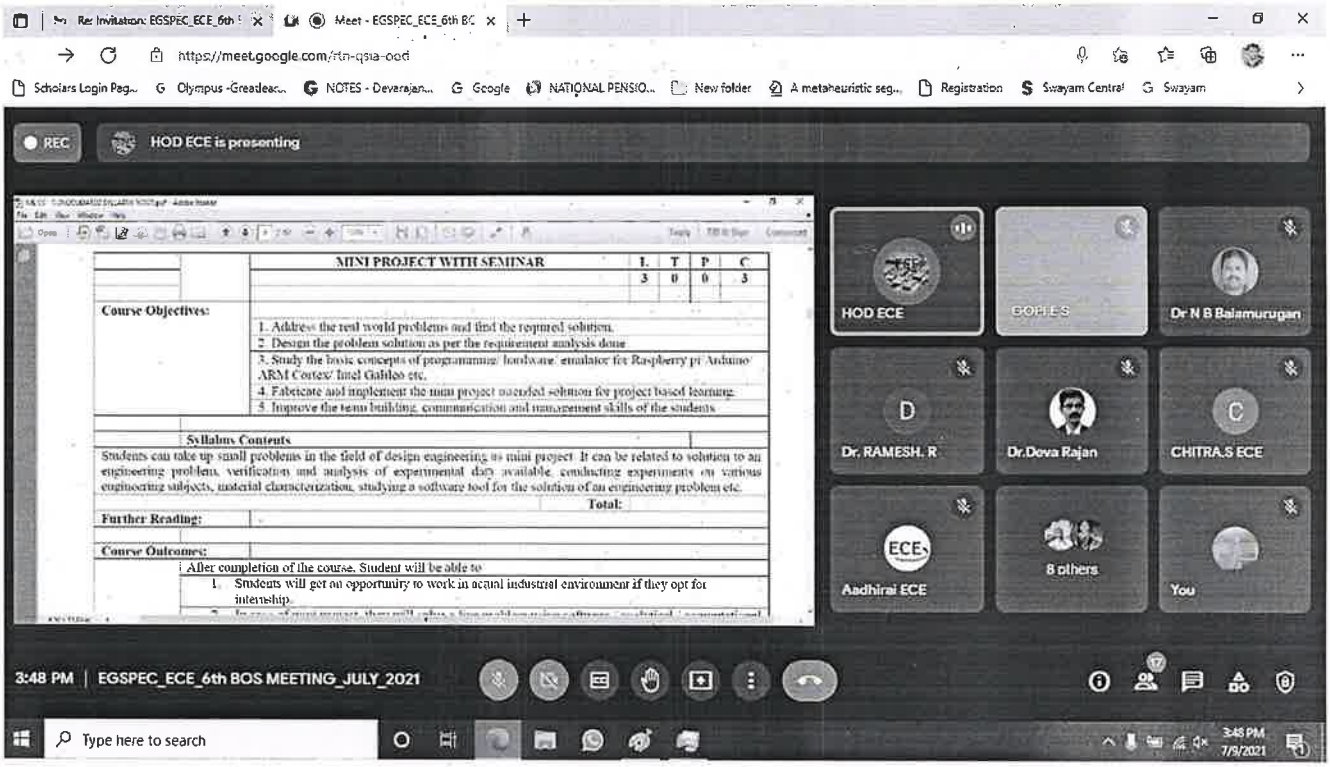
CHAIRMAN
Board of Studies
Department of Electronic &
Communication Engineering
EGS Pillay Engineering College (Autonomous)
Nagapattinam.



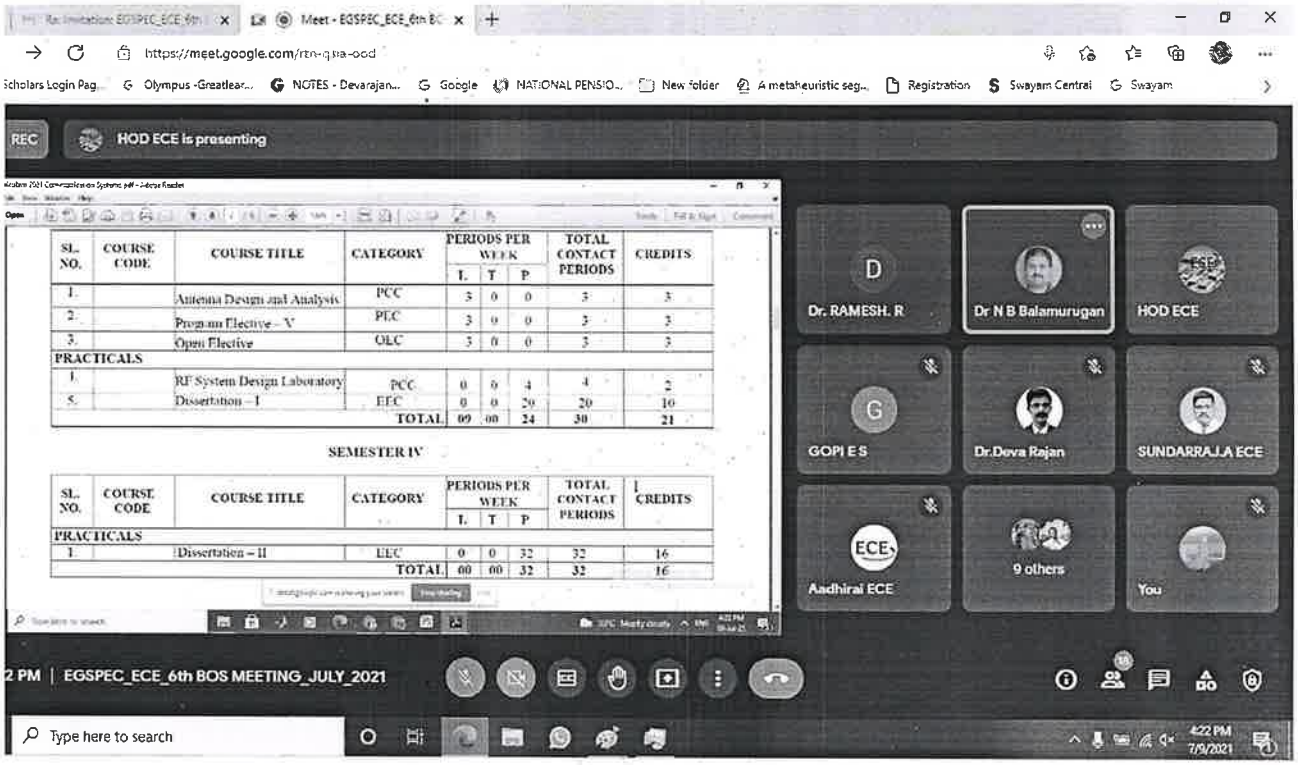
CHAIRMAN
Board of Studies
Department of Electronic & Communication Engineering
EGS Pillay Engineering College (Autonomou
Nagapattinam.



CHAIRMAN
Board of Studies
Department of Electronic & Communication Engineering
EGS Pillay Engineering College (Autonomous)
Nagapattinam.



CHAIRMAN
Board of Studies
Department of Electronic & Communication Engineering
EGS Pillay Engineering College (Autonomous)
Nagapattinam.

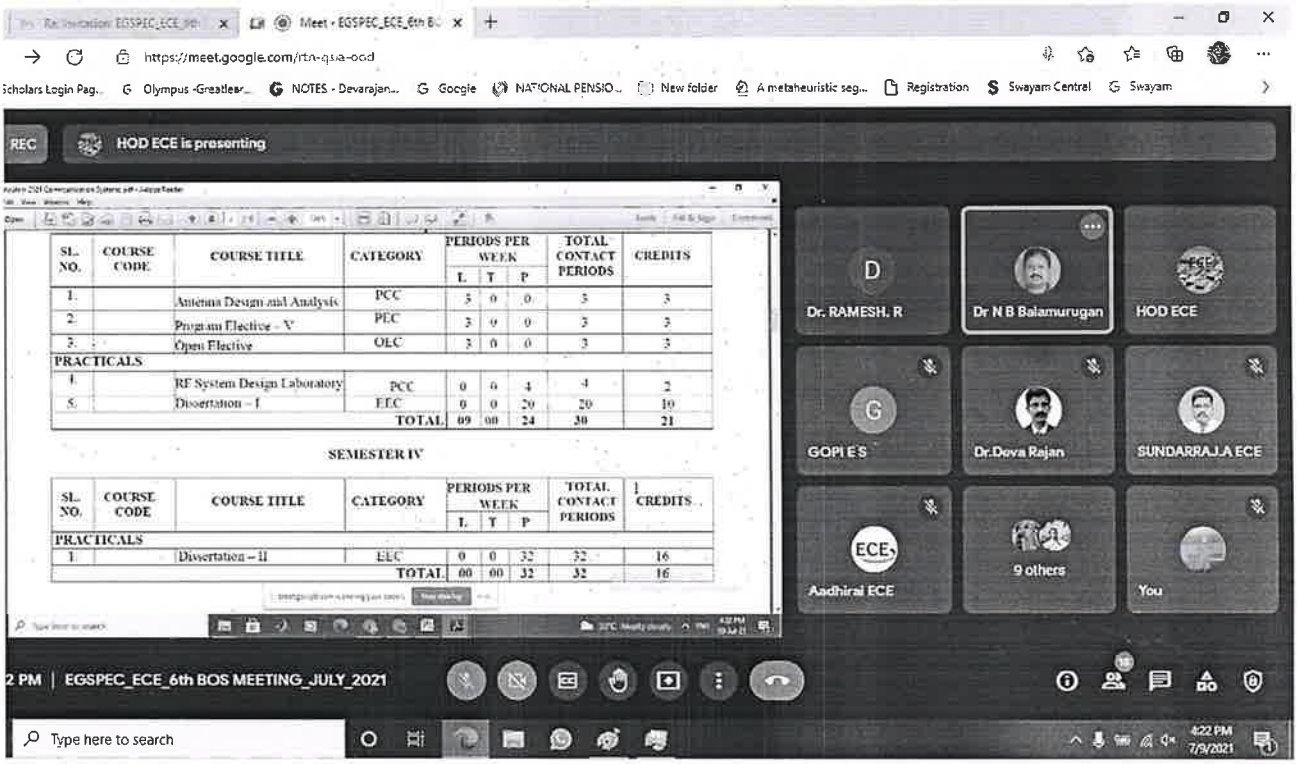


SL. NO.	COURSE CODE	COURSE TITLE	CATEGORY	PERIODS PER WEEK			TOTAL CONTACT PERIODS	CREDITS
				L	T	P		
1.		Antenna Design and Analysis	PCC	3	0	0	3	3
2.		Program Elective – V	PEC	3	0	0	3	3
3.		Open Elective	OEC	3	0	0	3	3
PRACTICALS								
1.		RF System Design Laboratory	PCC	0	0	4	4	2
5.		Dissertation – I	EEC	0	0	20	20	10
			TOTAL	09	00	24	30	21

SEMESTER IV

SL. NO.	COURSE CODE	COURSE TITLE	CATEGORY	PERIODS PER WEEK			TOTAL CONTACT PERIODS	CREDITS
				L	T	P		
PRACTICALS								
1.		Dissertation – II	EEC	0	0	32	32	16
			TOTAL	00	00	32	32	16

CHAIRMAN
Board of Studies
Department of Electronic & Communication Engineering
EGS Pillay Engineering College (Autonomous)
Nagapattinam.

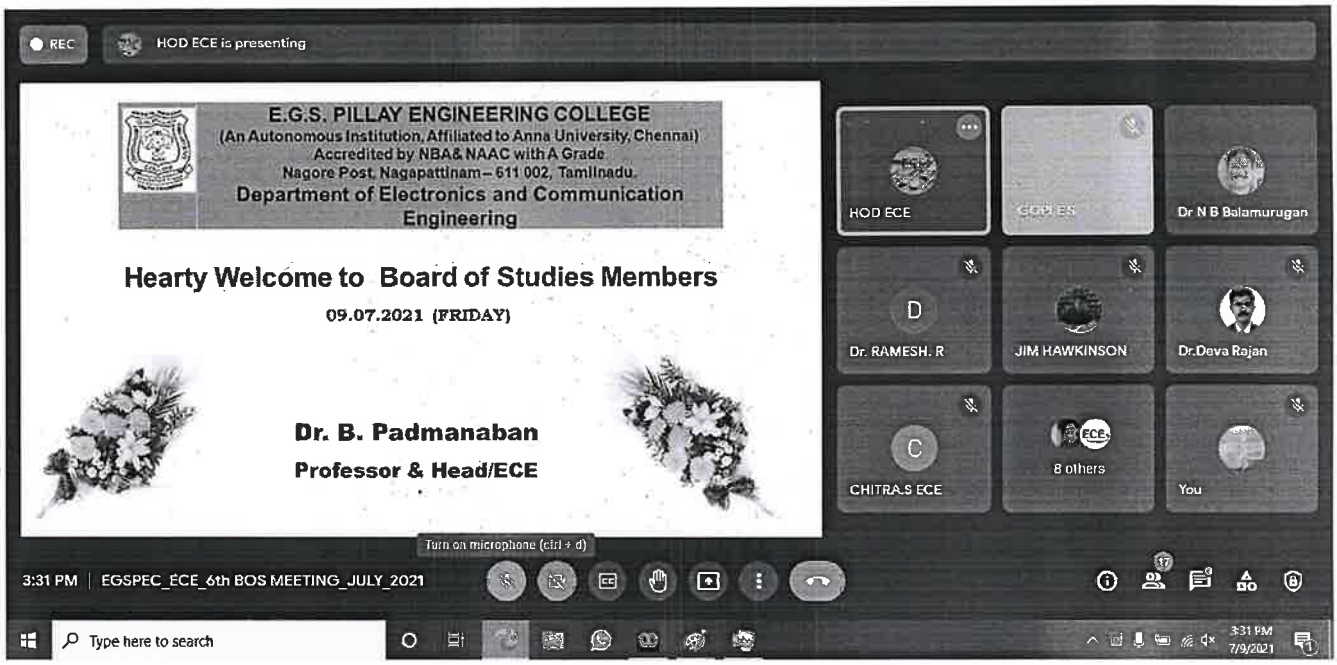


SL. NO.	COURSE CODE	COURSE TITLE	CATEGORY	PERIODS PER WEEK			TOTAL CONTACT PERIODS	CREDITS
				L	T	P		
1.		Antenna Design and Analysis	PCC	3	0	0	3	3
2.		Program Elective – V	PEC	3	0	0	3	3
3.		Open Elective	OEC	3	0	0	3	3
PRACTICALS								
4.		RF System Design Laboratory	PCC	0	0	4	4	2
5.		Dissertation – I	EEC	0	0	20	20	10
			TOTAL	09	00	24	30	21

SEMESTER IV

SL. NO.	COURSE CODE	COURSE TITLE	CATEGORY	PERIODS PER WEEK			TOTAL CONTACT PERIODS	CREDITS
				L	T	P		
PRACTICALS								
1.		Dissertation – II	EEC	0	0	32	32	16
			TOTAL	00	00	32	32	16

CHAIRMAN
Board of Studies
Department of Electronic & Communication Engineering
EGS Pillay Engineering College (Autonomous)
Nagapattinam.



CHAIRMAN
Board of Studies
Department of Electronic & Communication Engineering
EGS Pillay Engineering College (Autonomous)
Nagapattinam.

CHAIRMAN
Board of Studies
Department of Electronic & Communication Engineering
EGS Pillay Engineering College (Autonomous)
Nagapattinam.